

# STAY AHEAD BY BUILDING ON THE PAST!

## What is it?

The DL305 series is a small modular PLC that has been marketed by various name-brand PLC manufacturers for over 22 years. This Koyo design revolutionized the small PLC market, and is still a good performer and a great value.

## What's it got?

- Three standard CPUs to choose from, including the D3-350 with PID control and two communication ports
- Specialty CPUs to convert a DL305 system into an RTU for Optomux, Pamux, or to allow BASIC programming for custom applications
- Five, eight and 10-slot bases
- 110/220VAC or 24VDC power supply
- AC, DC inputs
- AC, DC, and relay outputs
- Eight or 12-bit analog input/output
- Specialty modules such as high-speed counter, and communication interface module (work with D3-330 and D3-340 CPUs)

## What can I do with it?

- Maintain or upgrade any installed GE Series One, Texas Instruments Series 305, or Siemens SIMATIC TI305 system
- Build a system that meets Class I Division II classification
- Build RTU I/O stations for a host computer with a serial or parallel interface
- Build an intelligent RTU that programs in the BASIC language

## You can swap parts among all the PLCs on the opposite page!

The DL305 PLC design has offered exceptional reliability, the right mix of features and a great price. This design was so well-liked when it was introduced that it became the most popular privately-labeled PLC in history. Best of all, almost every part produced in its 22-year history is interchangeable!

## GE nicknamed it “America’s Most Popular PLC”

In 1983, General Electric decided to private label the Koyo SR21 design. They called it the General Electric Series One and changed the color from Koyo yellow to black. The product sold so well they nicknamed it “America’s Most Popular PLC.”

## Texas Instruments called it the “Best Value PLC”

In 1989, Texas Instruments began to private label the Koyo SR21 design. They named it the Series 305 and changed the plastic color to light gray. The product was enhanced and did so well for Texas Instruments that they nicknamed it the “Best Value PLC.”

## Siemens also called it the “Best Value PLC”

In 1991, Siemens Industrial Automation decided to private label the Koyo SR21 design. They named it the SIMATIC TI305 and changed the color to charcoal grey. Once again, the product line continued to receive enhancements and the nickname “Best Value PLC” continues at Siemens.

## AutomationDirect calls it a “PLC Classic” and makes it state-of-the-art

In 1994, AUTOMATIONDIRECT added more enhancements with Windows-based *DirectSOFT*, and in 1997 we introduced the D3-350 CPU, which offers state-of-the-art features, while maintaining compatibility.

## Credibility in numbers

The Koyo DL305 design has been one of the most widely marketed PLCs in history. Over three million modules have been sold, proving that these modules are extremely reliable and well-suited for many applications and they cost less from us now than they ever did from any of the guys who previously marketed this solid PLC line. Our customers tell us (with their dollars) that the DL305 line is still a workhorse in the industry.





**Koyo Module**   **GE**   **Texas Instruments Module**   **Siemens Module**   **DirectLogic D3-350-**   **GE Base**

## PLC CPUs: DL305

CPU Feature	D3-330	D3-340	D3-350
Memory (words)	3.7K	3.7K	14.8K
Local I/O	176	184	368
Remote I/O	NO	NO	512**
RS-232C ports	1*	2	2
Network master	NO	YES	YES
MODBUS RTU	NO	Slave	Master/slave
Built-in remote I/O	NO	NO	YES
Real-time clock	NO	NO	YES
PID	NO	NO	YES
Floating-point math	NO	NO	YES
Drum timers	NO	NO	YES
Price	↔	↔	↔

\*D3-DCU required \*\*Uses DL205 and DL405 remote slaves

# FIELD TESTED FOR OVER 22 YEARS!

# 1983

**D3-350 CPU is a spin-off of our most powerful CPUs: the D4-450 and D2-250 CPU**



The D3-350 CPU has a killer instruction set (compatible with the DL405, DL205, DL105, DL06 and DL05 families) and some super practical communications. The D3-350 CPU can use a simplified I/O addressing method, and can access up to twice as much local I/O as any other DL305 configuration (when installed in a system with “-1” bases).

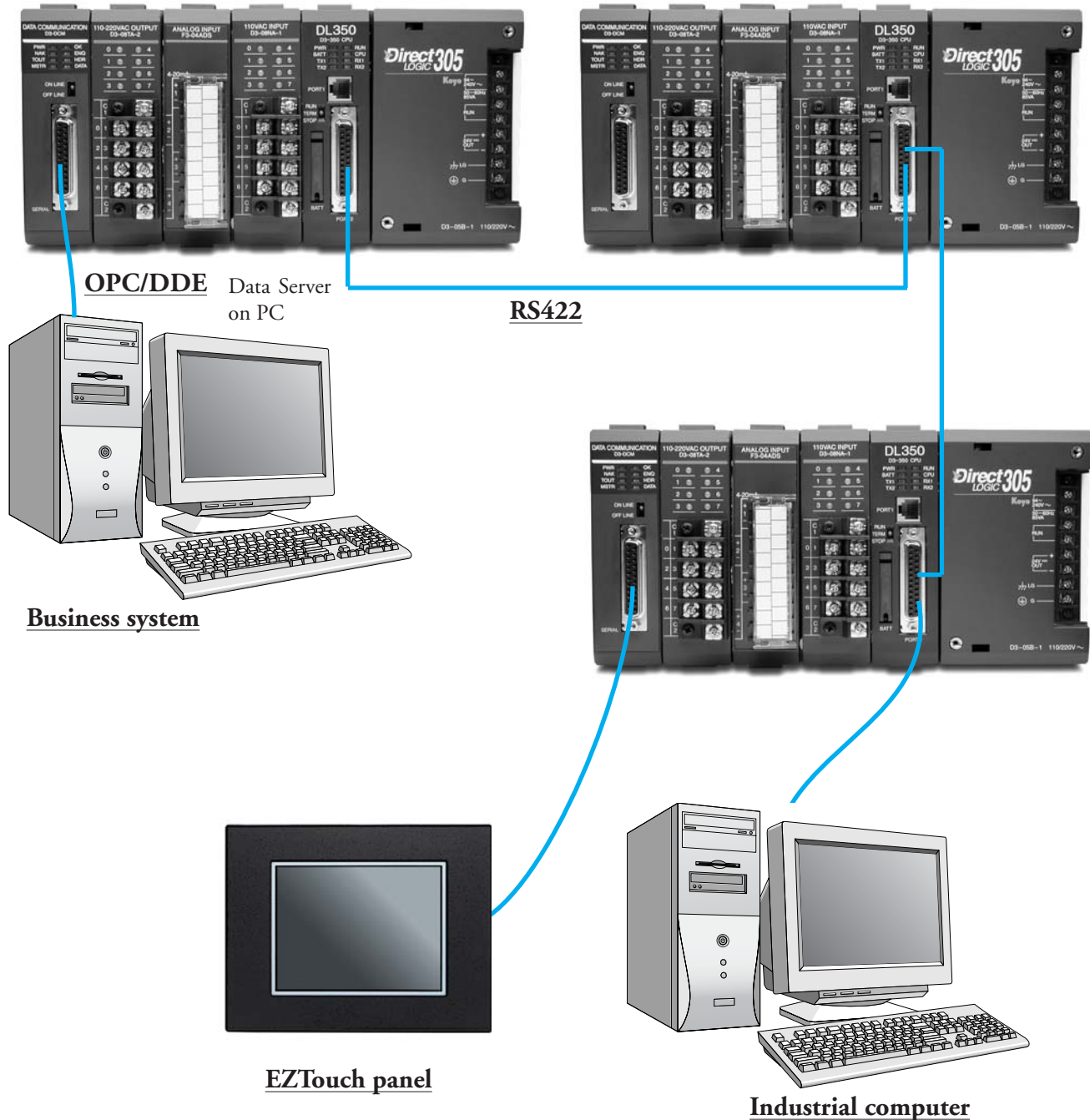
**Order online or call 1-800-633-0405 for replacement modules: Maintain or upgrade any installed GE Series One, Texas Instruments Series 305, or Siemens SIMATIC TI305 system**



# 2005

# DL305 FAMILY OF PRODUCTS

## DL305 system example with serial communications network and operator interface



# DL305 FAMILY OF PRODUCTS

The following is a quick summary of the DL305 family of products. The DL305 products have been sold by previous vendors under a wide variety of part numbers. A complete list of product offerings with vendor cross-reference is available in the DL305 price list.

## CPU's

**D3-350** –14.8K total memory  
2 communication ports  
4 PID loops  
MODBUS Master/Slave  
Remote I/O  
Floating point math

**D3-340** – 3.7K total memory  
2 communication ports

**D3-330** – 3.7 K total memory

## Specialty CPUs

### F3-OMUX-1

Serial interface to Optomux host  
2 communication ports  
(RS232C/422/485) selectable

### F3-OMUX-2

Serial interface to Optomux host  
2 communication ports  
(RS422/485)

### F3-PMUX-1

Parallel interface to Pamux host

## Bases

5-slot local or expansion base  
Built-in 110/220 VAC power supply  
5-slot local or expansion base  
Built-in 24 VDC power supply  
8-slot local base  
(exp. base w/350 CPU)  
Built-in 110/220 VAC power supply  
10-slot local or expansion base  
Built-in 110/220 VAC power supply  
10-slot local or exp. base  
Built-in 24 VDC power supply



## Discrete input modules

### DC Input

8-pt. 24VDC source  
16-pt. 5V/12-24VDC (sink/source, 1ms response)  
16-pt. 24VDC source (0.8ms response)

### AC Input

8-pt. 110/220VAC  
16-pt. 110 VAC

### AC/DC Input

8-pt. 24VAC/DC  
16-pt. 24VAC/DC

## Discrete output modules

### DC Output

4-pt. 5-24VDC sink  
8-pt. 5-24VDC sink  
8-pt. 5-24VDC source  
16-pt. 5-24VDC sink  
16-pt. 5-24VDC source

### AC Output

4-pt. 110-220VAC isolated  
8-pt. 110-220VAC  
8-pt. 110-220VAC isolated  
16-pt. 15-220VAC

### RELAY Output

8-pt. 5.0A/pt  
8-pt. 4.0A/pt isolated  
8-pt. 10.0A/pt isolated  
16-pt. 2A/pt

## Analog modules

4 Channel IN, 12 bit, isolated  
8 Channel IN, 12 bit  
8 Channel thermocouple  
16 Channel IN, 12 bit  
4 Channel OUT 12 bit  
4 Channel OUT 12 bit (isolated)

## Specialty modules

8 pt. Input Simulator  
Filler Module

## Programming

**D3-HP** Handheld Programmer for  
D3-330/D3-340

**D2-HPP** Handheld Programmer with built-in  
RLL<sup>PLUS</sup> for D3-350

## DIN rail mounted terminal blocks

See the Connection Systems section for over 200 available options.

## Communications

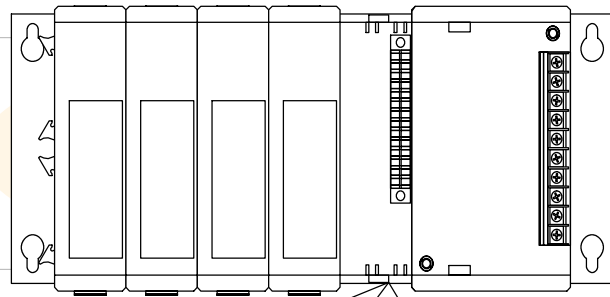
Data Comm Unit (RS232C), 330/340 CPUs only  
Data Comm Unit (RS422), 330/340 CPUs only  
Data Comm Module, 350 CPU only

## Operator panels

See the Operator Interface section for a complete listing of all types of panels and software.

# DL305 CPUs

There are three conventional CPUs and three specialty CPUs in the DL305 family. There are many considerations for choosing the right CPU, most of which depend on your particular application. The traditional CPUs, which offer control via RLL-style programming, are great for most applications. The information in this section provides a quick comparison. If you need to control I/O with a personal computer, or if you want to run a BASIC program in a CPU instead of ladder logic, then check out the specialty CPUs.



**D3-330** — The D3-330 design has been very popular for many years. It offers the lowest-cost solution in the DL305 family. It is great for machines that need little (if any) communications between the CPU and other devices.

**D3-340**—The D3-340 offers a faster scan rate, two RS232C ports (one with built-in MODBUS® RTU slave) and additional I/O points. Need RS422? Simply add an FA-ISOCAN converter to one of the ports. If you need built-in communications, or even just an extra 16-point I/O card, the D3-340 offers the lowest-cost solution. This CPU allows you to make the most of your investments in a DL305 (or compatible) system.

**D3-350**—The D3-350 is the most powerful DL305 CPU. It is a spin-off of our two most powerful CPUs, the D4-450 and D2-250(-1). It is plug-compatible with older bases, as well as analog and discrete I/O modules. The instruction set and I/O numbering scheme are similar to our DL05, DL06, DL105, DL205 and DL405 PLCs. The communications capabilities have also been greatly enhanced to now include RS422 Remote I/O, MODBUS Master and Slave protocols, as well as our own *DirectNet* and K-Sequence protocols. When the D3-350 is installed in a -1 base, even more features are available. These bases allow for greater I/O expansion capabilities and for future intelligent I/O modules.

**NOTE:** D3-330 and D3-340 programs cannot be downloaded into the D3-350 CPU. The D3-350's instruction set is based on the DL205/DL405 instruction set. If an existing D3-330 or D3-340 system is upgraded to a D3-350 CPU, the RLL program must be re-written for the D3-350 CPU.

# CPU SPECIFICATIONS

DL305 CPU Specifications			
System Capacity	D3-330	D3-340	D3-350
Total memory (K words)	3.91	3.98	14.8
Ladder memory (K words)	3.7	3.7	7.6
User data memory	116 bytes	172 bytes	7.1K words
CMOS RAM	Yes	Yes	No
UVPROM	Opt.	Opt.	No
EEPROM	No	Opt.	Flash
Total I/O points using:			
Local I/O	128	136	144
Local and Expansion I/O	176	184	368
Remote I/O <sup>1</sup>	N/A	N/A	512
I/O point density	8/16	8/16	8/16
Slots per base (CPU requires 1 slot)	5/8/10	5/8/10	5/8/10
Performance			
Contact execution (boolean)	6.6µs	.87µs	.61µs
Typical scan (1K boolean) <sup>2</sup>	15ms	4-5ms	5-6ms
Programming & Diagnostics			
RLL ladder style	Yes	Yes	Yes
RLL <sup>PLUS</sup> (stage)	No	No	Yes
RunTime Editing	No	No	Yes
Variable/fixed scan	variable	variable	either
Handheld programmer port	Yes	Yes	Yes
Built-in RS232C ports	No <sup>3</sup>	2	2
Real-time clock/calendar	No	No	Yes
Instructions	61	63	129
Control relays(CR)	140	196	1024
Shift register bits	128	128	use CRs
Stages (RLL <sup>PLUS</sup> only)	N/A	N/A	1024
Timers/counters	64	64	256/128
Immediate I/O	No	No	Yes
Subroutines	No	No	Yes
For/Next Loops	No	No	Yes
Timed interrupt	No	No	Yes
Integer math	Yes	Yes	Yes
Floating point math	No	No	Yes
PID	No	No	Yes
Drum sequence	No	No	Yes
Bit of word	No	No	Yes
ASCII print	No	No	Yes
Data registers	128	192	7168
Internal diagnostics	Yes	Yes	Yes
Password security	Yes	Yes	Multi-level
Battery backup	Yes	Yes	Yes
Communications			
Built-in ports <sup>3</sup>	No	Yes	Yes
DirectNET master	No	Yes	Yes
DirectNET slave	w/DCU	Yes	Yes
MODBUS RTU master	No	No	Yes
MODBUS RTU slave	No	Yes	Yes
Data communications unit	Yes	Yes	N/A
Specialty modules			
Thermocouple	Yes	Yes	Yes
Analog Input (#channels max.)	112	128	368
Analog output (#channels max.)	28	32	48
High-speed counter (10KHz)	Yes	Yes	No



1. The D3-350 bottom port supports DL205 remote I/O.
2. 1K program includes contacts, coils, and scan overhead. If you compare to other products, make sure to include their scan overhead.
3. The D3-330 requires a Data Communications Unit (DCU) for programming with DirectSOFT32 software.

# D3-350 <--->

## D3-350, our most powerful DL305 CPU

The D3-350 combines the power, speed and ease of the D2-250-1 CPU with existing DL305 I/O modules and bases. With such an incredible array of features, you may be able to replace more expensive PLCs.

*DirectSOFT32* Programming Software Release V2.3 or higher is required to program the D3-350. For existing license holders, an upgrade package is available. If you are using a handheld programmer (D2-HPP, release 1.8 or lower), a new release of handheld programmer firmware will also be required.

## Four PID loops and auto-tuning

The D3-350 CPU can process up to four PID loops directly in the CPU. Select from various control modes, including automatic, manual and cascade control. There are a wide variety of alarms including Process Variable, Rate of Change and Deviation. The loop operation parameters (Process Variable, Setpoint, Setpoint Limits, etc.) are stored in V-memory, which allows easy access from operator interfaces or HMIs.

Setup is accomplished with easy-to-use setup menus and monitoring views in our *DirectSOFT32* Programming Software.

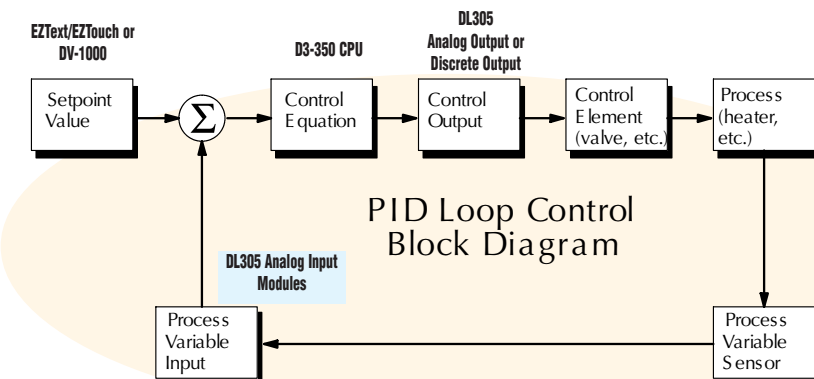
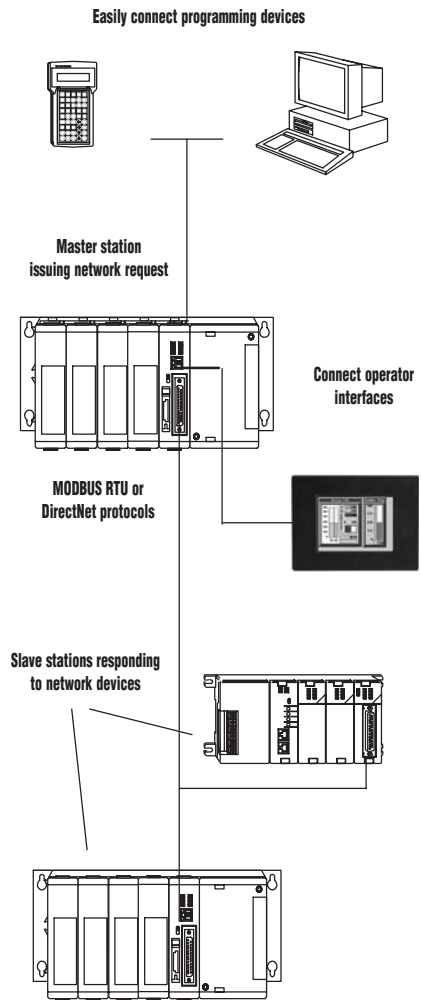
The auto-tuning feature is also easy to use and can reduce setup and maintenance time. The CPU uses the auto-tuning feature to automatically determine near optimum loop settings.

**Note:** D3-330 and D3-340 programs cannot be downloaded into the D3-350 CPU. The D3-350's instruction set is based on the DL205/DL405 instruction set. If an existing D3-330 or D3-340 system is upgraded to a D3-350 CPU, the RLL program must be re-written for the D3-350 CPU.



## Powerful built-in CPU communications

The D3-350 offers two communication ports that provide a vast array of communication possibilities. The top RS232C port is for programming, a DV-1000 connection, a connection to our operator interface panels, or a K-sequence/*DirectNET* slave port. The 25-pin bottom port can use RS232C or RS422. This port offers several different protocol options, such as K-sequence protocol, *DirectNET* Master/Slave, MODBUS RTU Master/Slave, and even a direct connection to DL205 remote I/O. The ability to select these features is provided via software so you can choose the best combination for the application.



# D3-350 KEY FEATURES

The D3-350 supports over 130 powerful instructions, many of which are new to the DL305 family. These include:

- Four types of drum sequencers
- Leading and trailing edge triggered one-shots
- Bit of word manipulation
- Floating point conversions
- Print instruction to send ASCII data through the bottom CPU port

For a complete list of instructions supported by the D3-350 CPU, see the end of this section.

## On-board flash memory

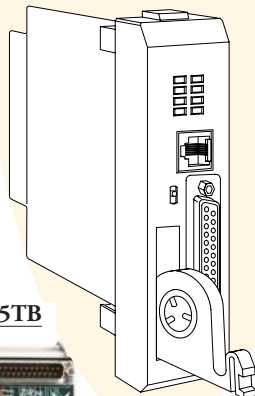
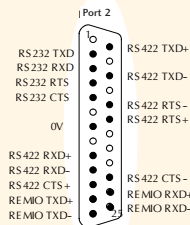
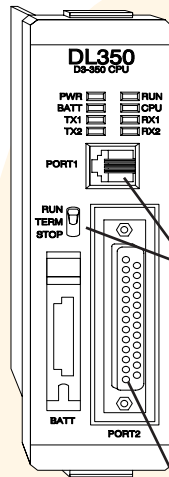
The D3-350 has 7.6 K of flash memory on board. With flash memory, you don't have to worry about losing the program due to a bad battery. If you have critical data stored in V-memory, simply purchase the optional lithium battery to maintain these parameters as well.

## Built-in remote I/O connection

The bottom port on the D3-350 can also be used as a master for a remote I/O network. If you need extra I/O at some remote distance from the CPU, use this port to add up to seven DL205 remote slave stations. (See the DL205 section for D2-RSSS information.)

## ZIPLink communications adapter modules

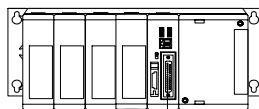
ZIPLink cables and communications adapter modules offer fast and convenient screw terminal connections for the D3-350 lower port. They are RS232/422 DIP switch selectable. See the ZIPLink connection systems section in this catalog for part numbers and descriptions.



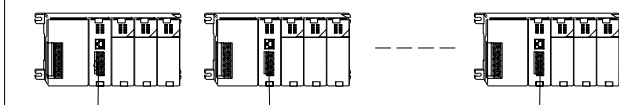
DN-25TB



Built-in Remote I/O master



...allows you to connect up to seven DL205 remote slaves!



CPU Status Indicators		
<b>RUN</b>	ON	CPU is in RUN mode
	OFF	CPU is in Program mode
<b>BATT</b>	ON	Battery backup voltage is low
	OFF	Battery backup voltage is OK or disabled
<b>CPU</b>	ON	CPU internal diagnostics has detected an error
	OFF	CPU is OK
<b>PWR</b>	ON	CPU power good
	OFF	CPU power failure
Mode Switch		
<b>RUN</b>	Forces CPU into Run Mode	
<b>TERM</b>	Allows peripherals (HPP, <i>Direct</i> SOFT32 and operator interface panels) to write to the CPU.	
<b>STOP</b>	Forces CPU out of RUN mode	
Port 1		
<b>Protocols</b>	K-sequence slave <i>Direct</i> NET slave	
<b>Devices</b>	Can connect w/HPP, <i>Direct</i> SOFT32, DV-1000, EZTouch/EZText Panels, or any <i>Direct</i> NET Master	
<b>Specs.</b>	6P6C phone jack connector RS232C 9600 baud Odd parity Fixed station address 1 8 data bits 1 start, 1 stop bit Asynchronous, half-duplex, DTE	
Port 2		
<b>Protocols</b>	K-sequence slave <i>Direct</i> NET Master/slave MODBUS RTU Master/slave Remote I/O Master	
<b>Devices</b>	Can connect w/many devices, such as PCs running <i>Direct</i> SOFT32, <i>KEP direct</i> for PLCs Server, HMI packages, DV-1000, EZTouch/EZText panels, or any <i>Direct</i> NET or MODBUS RTU master or slave	
<b>Specs.</b>	25-pin D-shell connector RS232C/RS422 300/600/1200/2400/4800/9600 19.2K/38.4K Baud Odd, even or no parity Selectable address (1-90, HEX 1-5A) 8 data bits-1 start, 1 stop bit Asynchronous, half-duplex, DTE	
Batteries (optional)		
<b>D2-BAT-1</b>	D3-350 only, coin type 3.0V Lithium battery, 560mA battery # CR2354	

**Note: Batteries are not needed for program backup. However, you should order a battery if you have parameters in V-memory that must be maintained in case of a power outage.**



# D3-350 PID LOOP SPECIFICATIONS

PID Loop Specifications and Key Features	
<b>Number of Loops</b>	Selectable, four maximum
<b>CPU V-Memory Required</b>	32 V-memory locations per loop selected (additional 32 V-memory locations per loop required if using Ramp/Soak)
<b>PID Algorithm</b>	Position or velocity form of the PID equation. direct or reverse acting, square root of the error and error squared control.
<b>Auto Tuning</b>	Open-loop step response method and closed-loop limit cycle method.
<b>Sample Rate</b>	Specify the time interval between PV samples, 0.05 to 99.99 seconds. Smallest sample rate is limited to either 0.05 seconds or (PLC scan time x number of loops).
<b>Loop Operation Modes</b>	Loops can be in automatic control, manual (operator) control, or cascade control. PV alarm monitoring continues when loops are in manual mode.
<b>Ramp/Soak</b>	Up to 16 steps (8 ramp, 8 soak) per loop, with indication of ramp/soak step.
<b>Square Root PV</b>	Specify a Square root of the PV for a flow control application.
<b>Limit SP</b>	Specify a maximum and minimum value for allowable setpoint changes.
<b>Limit Output</b>	Specify a maximum and minimum value for the output range.
<b>Gain</b>	Specify proportional gain of 0.01 to 99.99.
<b>Reset</b>	Specify integral time of 0.1 to 999.8 in units of seconds or minutes.
<b>Rate</b>	Specify the derivative time, 0.00 to 99.99 seconds.
<b>Rate Limiting</b>	Specify a derivative gain limiting coefficient to filter the PV used in calculating the derivative term (0 to 20).
<b>Bumpless Transfer I</b>	Bias and setpoint are initialized automatically when the module is switched from manual to automatic. This provides for a bumpless transfer, which reduces the chance of sharp changes in the output as a result of entering automatic mode.
<b>Bumpless Transfer II</b>	Bias is set equal to the output when the module is switched from manual to automatic. This allows switching in and out of automatic mode without having to re-enter the setpoint.
<b>Error Deadband</b>	Specify an incremental value above and below the setpoint in which no change in output is made.
<b>Error Squared</b>	Squaring the error minimizes the effect a small error has on the Loop output, however both Error Squared and Error Deadband control may be enabled.
Alarm Specifications	
<b>Deadband</b>	Specify 0.1% to 5% alarm deadband on all alarms except rate of change.
<b>PV Alarm Points</b>	Specify PV alarm settings for low-low, low, high, and high-high conditions. You can also specify a deadband to minimize the alarm cycles when the PV approaches alarm limits.
<b>PV Deviation</b>	Specify alarms to indicate two ranges of PV deviation from the setpoint value (yellow and red deviation).
<b>Rate-of-Change</b>	Specify a rate-of-change limit for the PV.

# D3-330 AND D3-340 KEY FEATURES

**D3-330** <---->  
**D3-340** <---->

The diagram to the right shows the various hardware features found on the D3-330 and D3-340 CPUs.

## EEPROM and UVPROM chips

CPU Status Indicators		
<b>RUN</b>	ON OFF	CPU is in RUN mode CPU is in Program mode
<b>BATT</b>	ON OFF	Memory backup voltage low Memory backup voltage good
<b>CPU</b>	ON OFF	CPU failure CPU is good
<b>PWR (Power)</b>	ON OFF	CPU power good CPU power failure
<b>Port1 RX/TX (D3-340)</b>	RED GREEN	Flashing red indicates the CPU port is receiving data Flashing green indicates the CPU port is sending data
<b>Port2 RX/TX (D3-340)</b>	RED Green	Flashing red indicates the CPU port is receiving data Flashing green indicates the CPU port is sending data

The DL305 CPUs come with on-board RAM and a battery. If you need additional program security, you may want to choose the EEPROM or UVPROM memory.

### D3-CPU-UV <---->

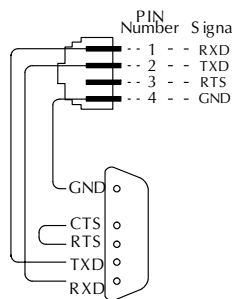
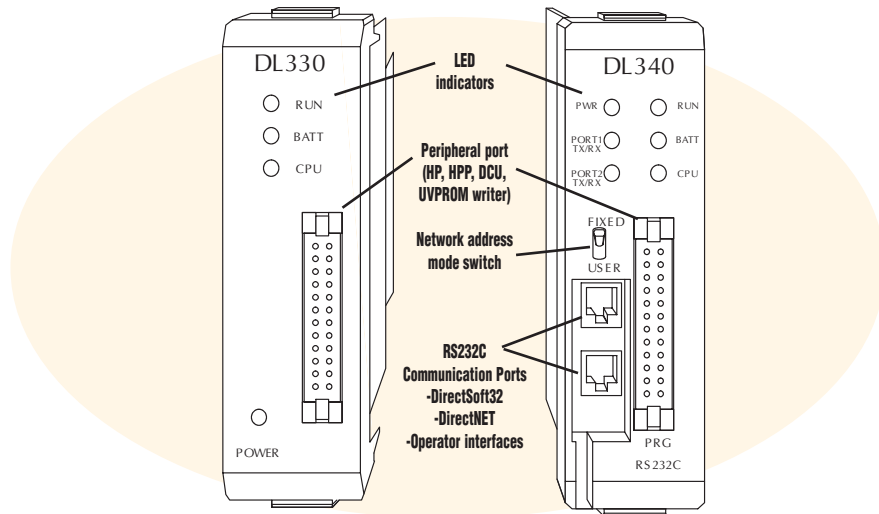
Optional UVPROM memory. Four chips per pack. (Only one chip is required for the CPU.) A D3-PWU Prom Writer Unit is required for programming.

### D3-340-EE <---->

Optional EEPROM memory for the D3-340 only. Four chips per pack. (Only one chip is required for the CPU.) No additional programming device is necessary.

### D3-D4-BAT <---->

Spare battery (lithium 3.0V). Also used for D4-430 and D4-440 CPUs.



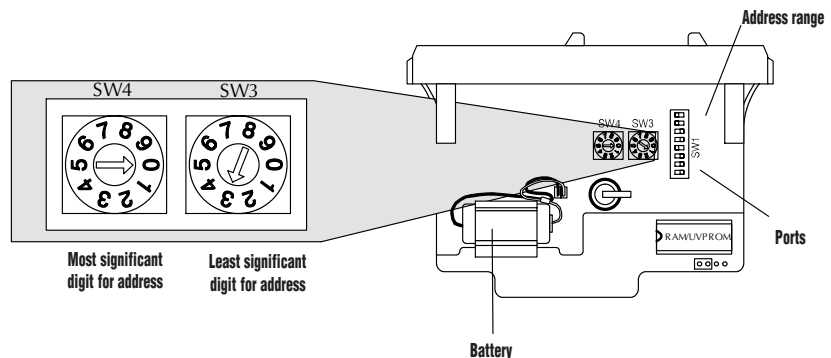
D3-340 RS232C Communication Port Specs	
Protocol	DirectNET
Connector	RJ11(handset connector)
Network address	01 to 90
Baud rate	38400, 19200, 9600, 4800, 2400, 1200, 600, 300
Parity-	None or odd
Transfer mode	HEX/ASCII Half-duplex Asynchronous
Data bits	8
Start bits	1
Stop bits	1
Turn around delay	0 to 1980 in 20ms intervals (preset with R777)

## Hardware switches

Below is a side view of a D3-340 CPU that reveals several types of hardware switches.

The D3-330 has a 2-position dipswitch for selecting retentive memory and jumpers for selecting UVPROM and RAM options.

The D3-340 has a jumper switch for selecting UVPROM, EEPROM and RAM options, two rotary switches to select network addresses and an eight-position dipswitch for selecting baud rates (300 to 38.4K baud), communication mode (slave, master, MODBUS® RTU) and memory options.



# DL305 SPECIALTY CPUs

Your application may require an unconventional PLC solution. For instance, you may need computer-controlled I/O (the PLC I/O is controlled directly by your personal computer), or maybe you would like a PLC that executes a control program written entirely in BASIC instead of RLL. AUTOMATIONDIRECT offers three specialty CPUs that provide solutions for each of these applications.

## Computer I/O CPUs

Three CPUs are available for the DL305 family that allow DL305 I/O (with DL305 bases) to function as computer-controlled I/O. These CPUs (F3-OMUX-1, F3-OMUX-2 and the F3-PMUX-1) are similar in functionality, but offer different communication options. Each CPU allows DL305 modules of most types (see restrictions on types) to be interfaced with a host computer. The entire control program for the DL305 I/O is executed on the host computer, which uses an OPTOMUX or PAMUX driver.

The following diagrams and charts show the various features found on the DL305 specialty CPUs:

### F3-OMUX-n

#### Communication port specifications

Interface	F3-OMUX-1	RS232C/422
	F3-OMUX-2	RS422/485 (isolated)
Connector	2-9pin D-sub sockets (female)	
Baud Rate	Port 1: 300, 1200, 2400, 4800, 9600, 9200, 38400, 57600, 115200	
	Port 2: 9600	
Protocol	OPTO 22 serial communications	

### F3-PMUX-1

#### Communication port specifications

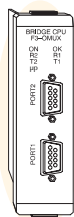
Interface	Parallel
Connector	50-pin ribbon cable connector
Protocol	OPTO 22 parallel communications

## I/O module restrictions

The specialty CPUs can make use of almost all DL305 modules, but they do not support the D3-HSC, or D3-02DA modules. These modules can only be used with the regular CPUs (D3-330 and D3-340).

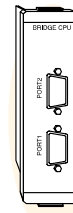
### F3-OMUX-1 <----> F3-OMUX-2 <---->

The F3-OMUX (-1, -2) CPU plugs into the first slot of a DL305 base. It acts as a serial interface to the control program in the host computer and up to 184 DL305 I/O per CPU. Multiple CPUs can be daisy-chained together to increase I/O count. The host computer must use an OPTOMUX serial communication driver. The host can execute a custom program or use a standard software package that supports OPTOMUX drivers such as Intouch-Wonderware, Iconics-Genesis, U.S. Data FactoryLink, Metra-Skyhawk Lt, etc.



### General Specifications

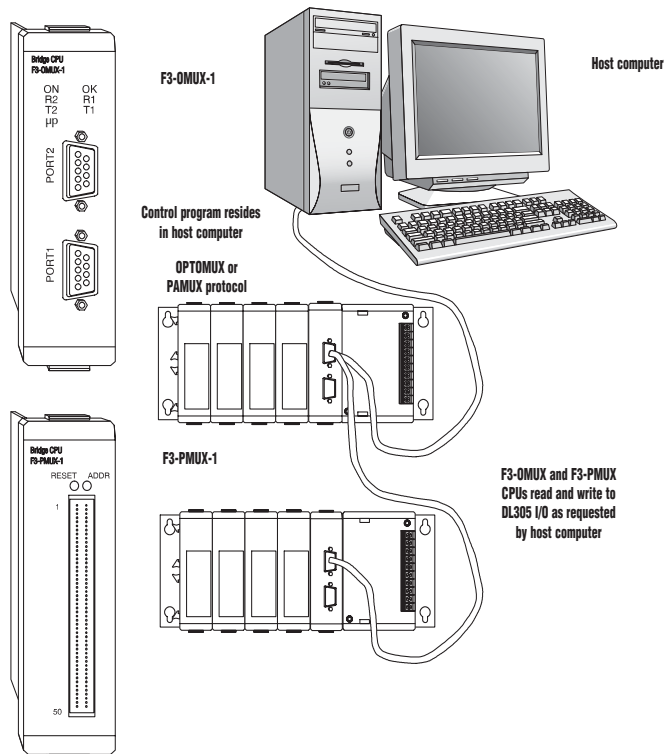
- Two serial ports that support the OPTOMUX protocol
- F3-OMUX -1**  
RS232C/RS422/RS485
- F3-OMUX-2**  
RS422/RS485 (isolated)
- Max of 184 I/O points per CPU (with expansion base unit)
- Scan time is dependent on the communication speed, number of commands sent, type of commands sent, the size of the response and the speed of the host computer.



### F3-PMUX-1 <---->

The F3-PMUX is similar in operation to the F3-OMUX (-1, -2). It uses a parallel interface instead of serial interface. As a result, it requires the host computer to use a PAMUX communication board (OPTO 22 part number AC28 or equivalent). With this board, you can use PAMUX communication drivers in your host software. Scan time constraints are similar to the OMUX units.

*The -1 version has a 26Mhz processor and replaces the F3-PMUX CPU.*

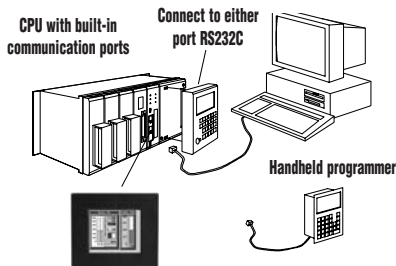


# COMMUNICATIONS

## Determine your communications requirements

The choice of CPU can have a big impact on your communications capabilities in the DL305 family. If you are considering doing any communications, you should use the D3-340 or the D3-350 CPUs. You can communicate with the D3-330 CPU, but you have to add a DL305 Data Communications Unit to connect any device other than a handheld programmer. The Data Communications Unit has only one port.

D3-340 RS232C Communication Port Specifications	
Protocol	DirectNET
Connector	RJ11(handset connector)
Network address	01 to 90
Baud rate	38400, 19200, 9600, 4800, 2400, 1200, 600, 300
Parity-	None or odd
Transfer mode	HEX/ASCII Half-duplex Asynchronous
Data bits	8
Start bits	1
Stop bits	1
Turn around delay	0 to 1980 in 20ms intervals (preset with R777)



Network Addresses		
Port	Protocol	Range
1	Slave	1-90
	Slave	1-90
2	Master	0
	MODBUS/RTU	1-247

## Standard communications

The D3-340 and D3-350 CPUs offer two built-in RS232C communication ports. Operator interfaces and *DirectSOFT32* can be connected to either port. On the D3-340 CPU, the handheld programmer is attached directly or with a cable to the parallel port adjacent to the two serial communication ports. On the D3-350 CPU, the handheld programmer is attached to Port 1. The handheld programmer can be operated simultaneously with the communication ports. The D3-340 baud rate and network addresses are set by hardware dipswitches and rotary switches for Port 1. Port 2 uses internal registers that can be changed with a handheld programmer or *DirectSOFT32*. Port 1 on the D3-350 is fixed. Port 2 can be configured using the handheld programmer or *DirectSOFT32*.

## DL305 as a slave on a network

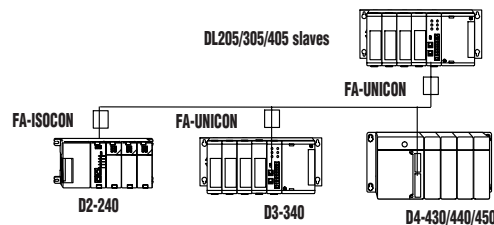
Both ports on the D3-340 and the D3-350 CPUs can serve as slave ports for *DirectNET*. The bottom ports offer additional flexibility in that they can serve as a slave on a MODBUS RTU network. The D3-350 even supports RS422, so no RS232-to-RS422 converter is needed. No programming is required in these CPUs for them to act as slave ports.

## DL305 as a network master

The bottom built-in communication port of the D3-340 and D3-350 CPUs can serve as a Network Master for *DirectNET*. Up to 90 slave stations can be addressed. The D3-350 can also serve as a MODBUS RTU Master; up to 247 slave stations can be addressed. DL405, DL305 and DL205 controllers can be used as slave stations. (Please note there are certain restrictions pertaining to valid DL205 and DL405 memory types that the D3-340 master can read and write.)

## Custom drivers

The DL305 product family supports the *DirectNET* protocol. However, in some applications you may have a need to connect to a device that does not support this protocol. If so, the ASCII/BASIC modules also allow you to write your own custom communication drivers (in BASIC) to connect to special devices. These high-speed modules offer communication rates up to 115.2K baud on RS232C, RS422, and RS485. With 128K of memory, there is ample program or data storage space. (These modules are not supported by the D3-350.)



# I/O SELECTION

## Choose your I/O modules

There are three major factors to consider when choosing an I/O module:

### Environmental specifications:

What environmental conditions will be present?

### Hardware specifications:

Does this product have the right features, performance and capacity to adequately serve the application?

### Field termination:

How does this module connect to field devices? For DC modules, is a sinking or sourcing module required?

## Environmental specifications

The adjacent table lists the environmental specifications that globally apply to the DL305 system (CPU, Bases, and I/O modules). Be sure the modules you choose are operated within these environmental specifications.

## Review I/O hardware specifications

The hardware specifications for every DL305 module are listed with each module. Discrete module specifications are shown in a format similar to the example to the right. Take time to understand the specification chart, the derating curve and the wiring diagram.

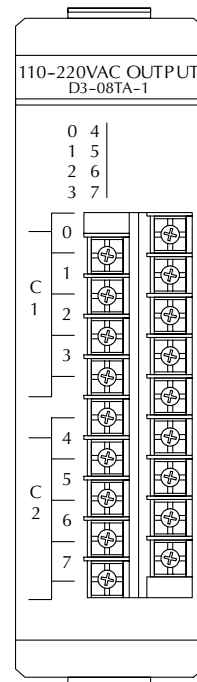
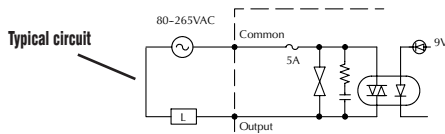
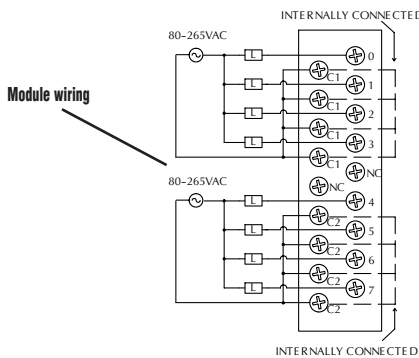
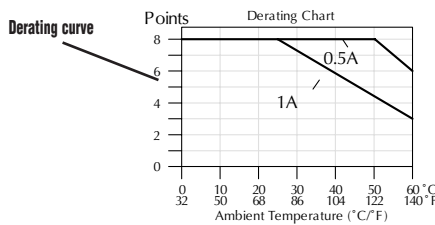
Specialty module specifications are shown in a format that is relevant for each particular module. These module specifications should help you determine if this module is right for your application.

## General I/O module specifications

Specification	Rating
Storage temperature	-4°F – 158°F (-20°C to 70°C)
Ambient operating temperature	32°F – 140°F (0° to 60°C)
Ambient humidity	5% - 95% relative humidity (non-condensing)
Vibration resistance	MIL STD 810C, Method 514.2
	Shifting: 0.075mm 10-57Hz 3 Axes
	Acceleration: 9.8 m/s <sup>2</sup> 57-150Hz 3 Axes
	Sweeping: 810C, Method 516.2
Peak accel.	147 m/s <sup>2</sup> 11ms, 3 Axes
Noise immunity	NEMA (ICS3-304)
Atmosphere	No corrosive gases

Discrete module specifications sheet example

D3-08TA-1 AC Output	
Outputs per module	8
Commons per module	2 (isolated)
Operating voltage	80-265VAC
Output type	TRIAC
Peak voltage	265VAC
AC frequency	47-63Hz
ON voltage drop	1.5 VAC @ 1A
Max current	1A/point 3A/common
Max leakage current	1.2mA @ 220VAC 0.52mA @ 110VAC
Max inrush current	10A for 16ms 5A for 100ms
Minimum load	25mA
Base power required	9V 20mA/ON pt. (160 MA Max) 24V N/A
OFF to ON response	1ms Max
ON to OFF response	8.33ms Max
Terminal type	Removable
Status indicators	Logic Side
Weight	7.4oz. (210g)
Fuses	2 (one 5A per common) Non-replaceable

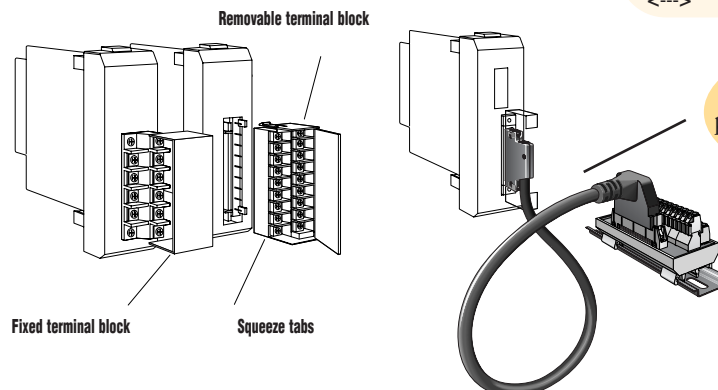


# I/O SELECTION

## Factors affecting field termination

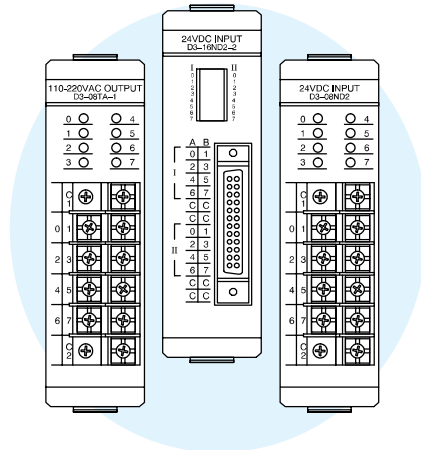
**Sinking and sourcing for DC field devices:** If you are using a DC type of field device, then you should consider whether the device is a sinking or sourcing configuration. This may affect your module selection since it determines the manner in which the device must be wired to the module. (Both sinking and sourcing modules are available.) Refer to the sinking/sourcing section of the Appendix for a complete explanation of how this could affect your system.

**Physical wire terminations:** In general, DL305 modules use five types of field terminations. They include: removable terminal blocks (included on most 8 and 16-point modules), fixed terminal blocks; specialty D-sub connectors (used on a few 16-point modules), standard D-sub connectors (used on most specialty intelligent modules), and phone jack style (used on the D3-340 CPU, some specialty modules and the universal cable kit). The module descriptions indicate the connector type that is on the module. The following illustrations shows these types of connectors. You can also use our DIN rail-mounted terminal blocks, DINnectors, or *ZIPLink* cables as a field termination interface to the PLC and I/O modules.



## Choose your modules

Now that you understand the factors that affect your choice of an I/O module, it's time to choose ones that best suit your needs. When you have selected the modules, proceed to the next section to choose an I/O configuration scheme that best suits your application.



## ZIPLink Connection System

The logo to the left is placed next to the I/O modules that are supported by the *ZIPLink* cable systems. (The modules are listed at the end of this section.) See the Connection Systems section in this desk reference for complete information on *ZIPLink* connection systems.

## Extra connectors or spare fuses

There are several types of spare parts that may be useful. A filler module provides a quick and easy way to cover empty slots. Or, it is sometimes helpful to have extra I/O module connectors or spare fuses. Also keep in mind the *DINector* family which provides DIN rail-mounted terminal blocks for simplifying and organizing your wiring needs.

- F3-FILL-CB – Filler module for empty slots <--->
- D3-16IOCON – 16pt. I/O terminal blocks <--->
- D3-8IOCVR – 8pt. I/O terminal plastic covers <--->
- D3-16IOCVR – 16pt. I/O terminal plastic covers <--->
- D3-IODSHEL – 24-pin D-shell connectors <--->
- D3-FUSE-1 – Fuses for D3-05B, D3-08B, and D3-10B <--->
- D3-FUSE-2 – Fuses for D3-04TAS <--->
- D3-FUSE-3 – Fuses for D3-05BDC <--->
- D3-FUSE-4 – Fuses for D3-08TAS and F3-16TA – 1 <--->
- D3-FUSE-5 – Fuses for D3-08TR <--->
- D3-FUSE-6 – Fuses for F3-08TRS-2 <--->
- D3-ACC-1 – Base power terminal strip screws <--->
- D3-ACC-2 – Spare terminal screws for 8pt. I/O modules <--->
- D3-ACC-3 – Spare terminal screws for 16pt. I/O modules <--->

# DL305 I/O CONFIGURATION

**Local I/O** – Local I/O are the modules that reside in the same base as the CPU. The status of each I/O point is updated on each I/O scan of the CPU.

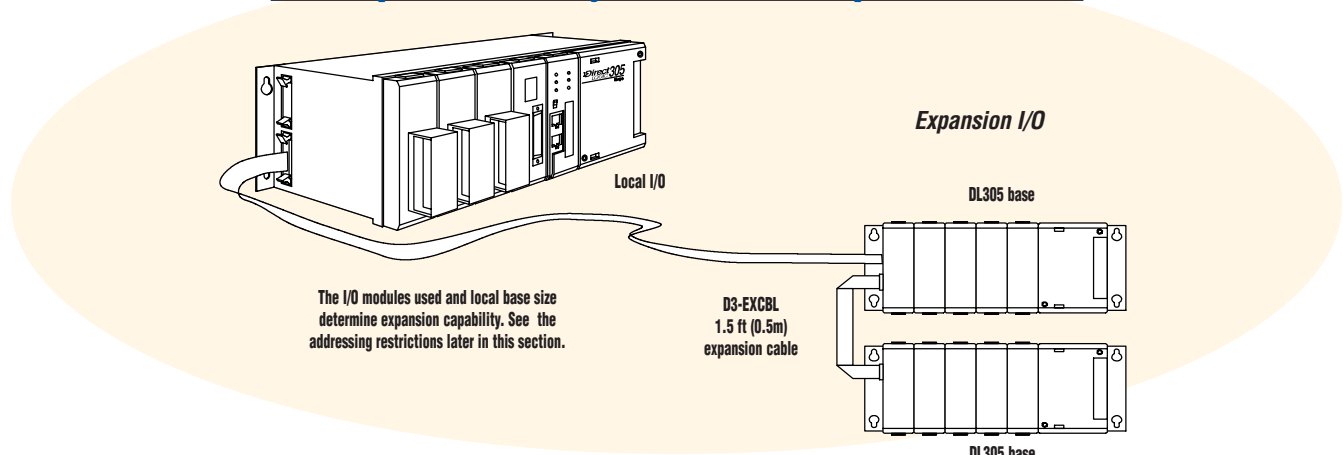
**Local expansion I/O** – Most local CPU bases can be expanded to include expansion I/O. Local expansion is commonly used when there are not enough I/O points available in the existing base configuration or the power budget maximum for the existing base will be exceeded with the addition of I/O. This configuration requires an additional base(s) and an I/O expansion cable(s). The CPU treats the expanded I/O in the same manner as local I/O, with updates every CPU I/O scan. There are certain addressing restrictions that are related to expansion I/O.

**Remote I/O** – (D3-350 CPU only) – Remote I/O is used when you need to place I/O bases at some remote distance from the CPU base. There are certain restrictions that are related to remote I/O. Check the catalog section on DL205 Remote I/O for examples and additional information.

I/O Configuration Limitations	D3-330	D3-340/ D3-350	D3-350 with -1 bases (AC powered only)
<b>5-slot Local CPU Base System</b>	64 I/O max	64 I/O max	64 I/O max
<b>5-slot Local CPU Base System with a 5-slot Expansion Base</b>	120 I/O max	128 I/O max	144 I/O max
<b>5-slot Local CPU Base System with two 5-slot Expansion Bases</b>	120 I/O max	128 I/O max	224 I/O max
<b>8-slot Local CPU Base System</b>	112 I/O max	112 I/O max	112 I/O max
<b>8-slot Local CPU Base System with a 5-slot Expansion Base</b>	152 I/O max	152 I/O max	192 I/O max
<b>8-slot Local CPU Base System with an 8-slot Expansion Base</b>	N/A	N/A	240 I/O max
<b>8-slot Local CPU Base System with an 8-slot Expansion Base &amp; 5-slot Expansion Base</b>	N/A	N/A	320 I/O max
<b>8-slot Local CPU Base System with two 8-slot Expansion Bases</b>	N/A	N/A	368 I/O max
<b>10-slot Local CPU Base System</b>	128 I/O max	136 I/O max	144 I/O max
<b>10-slot Local CPU Base System with a 5-slot Expansion Base</b>	168 I/O max	176 I/O max	224 I/O max
<b>10-slot Local CPU Base System with a 10-slot Expansion Base</b>	176 I/O max	184 I/O max	304 I/O max

*Note: The 16-point modules must be in the first eight slots adjacent to the CPU, rolling over into an expansion base if necessary.*

## Example of I/O system with expansion I/O



# I/O MODULE LOCATIONS

The design of the DL305 has a successful 22-year history. Each time the product family has grown or been enhanced, compatibility with the earlier products has been preserved to protect customer investments. This has resulted in an I/O numbering system and I/O location scheme that has some special requirements.

The Module Placement Guideline table explains the rules that pertain to module location. Some specialty modules have additional requirements. These are explained in their respective module data sheets. Remember that the power budget will limit the location where some modules can be placed in a base.

Module Placement Guidelines	
Device	Placement
<b>CPU</b>	<ul style="list-style-type: none"> <li>The CPU must reside in the first slot of the local CPU base (closest to the power supply).</li> <li>The CPU slot does consume an I/O slot. For example, a D3-05B-1 5-slot base has a slot for the CPU and 4 slots for I/O modules.</li> </ul>
<b>16 Point I/O Modules</b>	A maximum of eight 16-point modules may be installed in a system. However, the actual number allowed depends on the type of CPU you are using. <b>D3-330</b> - maximum of seven 16-pt. modules <b>D3-340/350</b> - maximum of eight 16-pt. modules <b>D3-350</b> - w/-1 base can have 16-pt. modules in all available slots
<i>Note: some specialty modules, such as the High Speed Counter and Thumbwheel Interface Unit, require 16-points and are treated as 16-point modules. The 16-point modules must be in the first eight slots adjacent to the CPU. They may roll over into an expansion base if necessary. If any of the eight slots adjacent to the CPU are not used for 16-point modules, they can be used for eight-point modules.</i>	
<b>Analog</b>	Analog modules must reside in any valid 16-point I/O module slot.
<b>ASCII BASIC Modules</b>	ASCII BASIC modules can be placed in any valid 16-point I/O slot. (D3-350 does not support these modules)
<b>High Speed Counter</b>	A High-Speed Counter must be used in the first four I/O module slots in the local CPU base. (D3-350 does not support these modules)

I/O Points Usage Table for Modules					
The following table indicates the number of I/O points that are used by each module. Use this information to ensure your I/O configuration stays within the valid I/O count of your chosen CPU. *Use only with D3-350 CPU, "-1" base, not in slot 8.					
DC Input		DC Output		Relay Output	
D3-08ND2	8	D3-04TD1	4	D3-08TR	8
D3-16ND2-1	16	D3-08TD1	8	F3-08TRS-1	8
D3-16ND2F	16	D3-08TD2	8	F3-08TRS-2	8
D3-16ND3	16	D3-16TD1-1	16	D3-16TR	16
<b>AC Input</b>		D3-16TD2	16	<b>Specialty Modules</b>	
D3-08NA-1	8	<b>AC Output</b>		D3-08SIM	8
D3-08NA-2	8	D3-04TAS	4	D3-DCM	0*
D3-16NA	16	F3-08TAS	8	<b>Analog</b>	
<b>AC/DC Input</b>		D3-08TA-1	8	F3-04ADS	16
D3-08NE3	8	D3-08TA-2	8	F3-08AD-1	16
D3-16NE3	16	F3-16TA-1	16	F3-08THM-n	16
		F3-16TA-2	16	F3-16AD	16
				F3-04DA-1	16
				F3-04DAS	16



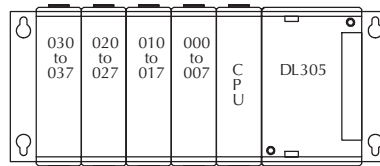
# DL305 ADDRESSING

## D3-330/340

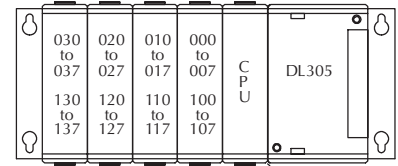
Like the DL205 and DL405 products, the DL305 uses octal addressing. That is, the I/O point addressing does not include any “8s” or “9s”. The DL305 is primarily different in that it uses slot addressing. That is, the addresses are assigned to the I/O slots and do not depend on the type of module installed (input vs. output). Also, the addresses are not sequential on 16-point modules. For example, a 16-point module in slot 0 (the first I/O slot) would have I/O addresses 000-007 for the first eight points and 100-107 for the next eight points.

There are also certain restrictions to consider when designing your system. Most of these situations arise when 16-point modules are used, or when expansion bases must be added to the system.

The diagrams on this and the following page illustrate the I/O base/addressing combinations that are possible when designing a system.

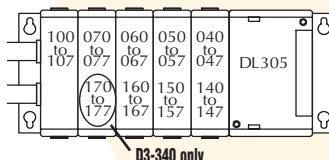
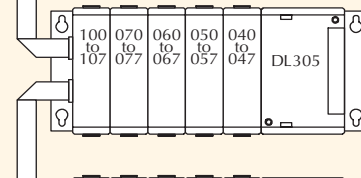
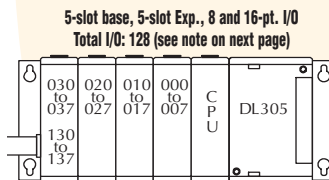
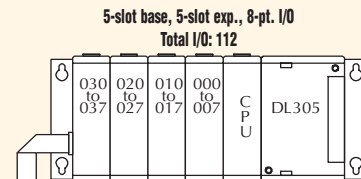
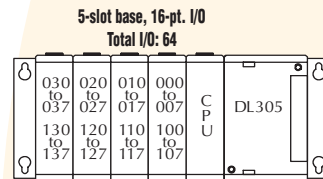
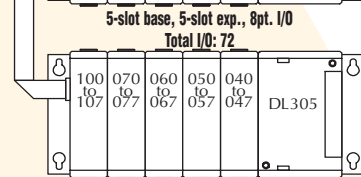
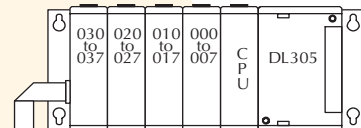
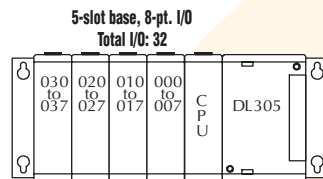


Slots: 3 2 1 0 CPU

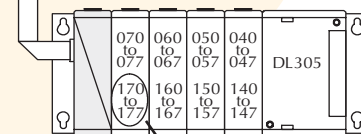
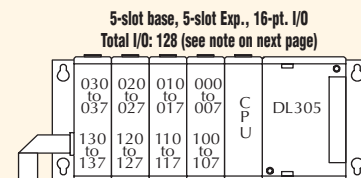
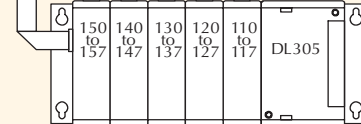
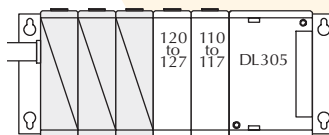


Slots: 3 2 1 0 CPU

## 5-slot base example configurations



D3-340 only

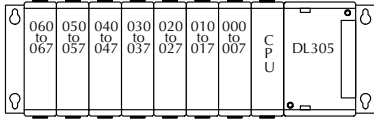


D3-340 only

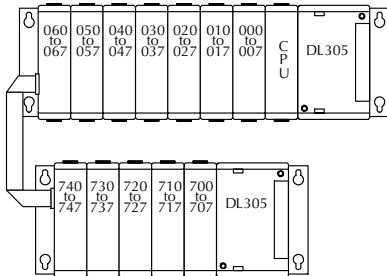
# DL305 ADDRESSING

## 8-slot base example configurations (D3-330/340)

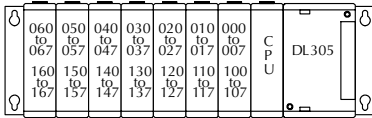
8-slot base, 8-pt. I/O  
Total I/O: 56



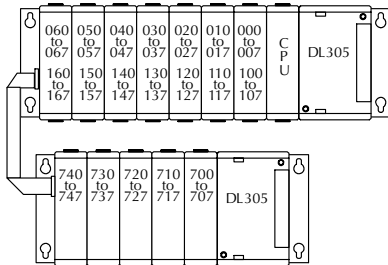
8-slot base, 5-slot expansion, 8-pt. I/O  
Total I/O: 96



8-slot base, 16-pt. I/O  
Total I/O: 112



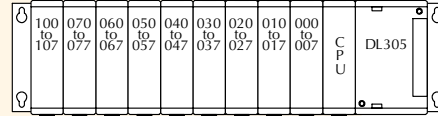
8-slot base, 5-slot expansion, 16-pt. I/O  
Total I/O: 156



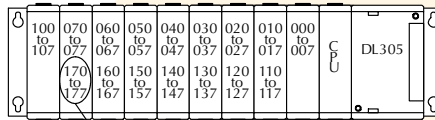
**\*NOTE:** Regardless of base size, if a 16-pt. module is used in Slot 6 for the D3-330 CPU, 160 through 167 will not be available for control-ready assignments. If a 16-pt. module is used in Slot 6 and/or Slot 7 for a D3-340 CPU, 160-167 and/or 170-177 are not available for control relay assignments.

## 10-slot base example configurations

10-slot base, 8-pt. I/O  
Total I/O: 72

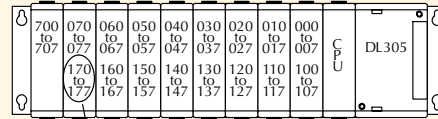


10-slot base, 8-pt. and 16-pt. I/O  
Total I/O: 128 (see note)



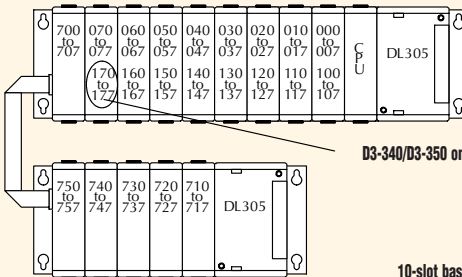
D3-340/D3-350 only

10-slot base, 8-pt. and 16-pt. I/O  
Total I/O: 136 (see note)



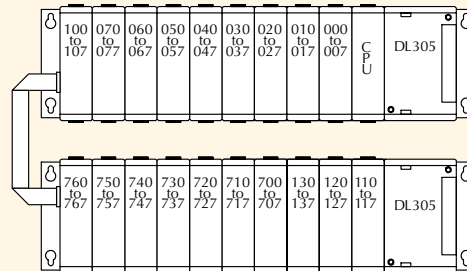
D3-340/D3-350 only

10-slot base, 5-slot expansion, 8-pt. and 16-pt. I/O  
Total I/O: 176 (see note)

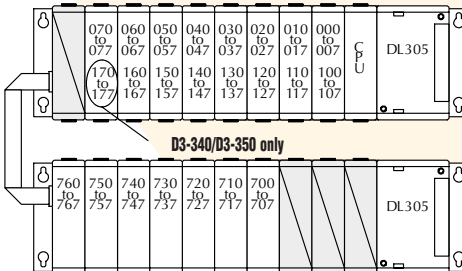


D3-340/D3-350 only

10-slot base, 10-slot expansion, 8-pt. I/O  
Total I/O: 152



10-slot base, 10-slot expansion, 8-pt. and 16-pt. I/O  
Total I/O: 184 (see note)



D3-340/D3-350 only

# D3-350 ADDRESSING

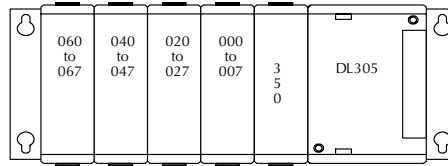
## Using “-1” bases

The D3-350 CPU can be installed in conventional DL305 bases or the “-1” bases. When installed in one of the conventional bases, or if the bases are mixed, the addressing scheme and module placement restrictions follow that of the D3-340 CPU. Refer to the previous pages for more detailed information. Note: These I/O addressing configurations are for the latest style bases (-1 on the end of the part number). If you are using an older series base, refer to the User’s Manual appendix for correct addressing.

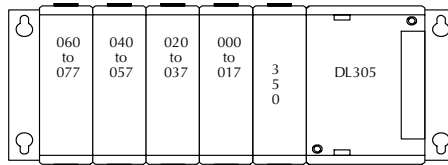
## I/O addressing

When the D3-350 CPU is installed in a “-1” base and all expansion bases are also “-1” bases, the addressing scheme is very simple. 16 I/O points are assigned to each slot. This applies even if the slot contains an 8-point module or if the slot is empty. Expansion base addresses follow in succession from the previous base. Input modules are assigned addresses X0 through X777. Output modules are assigned address Y0 through Y777.

D3-05B-1 base using 8-pt. I/O modules



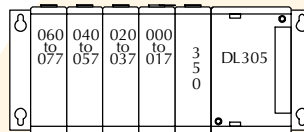
D3-05B-1 base using 16-pt. I/O modules



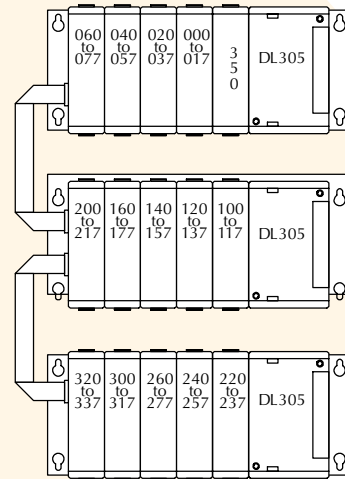
## 5-slot base example configurations

### -1 bases

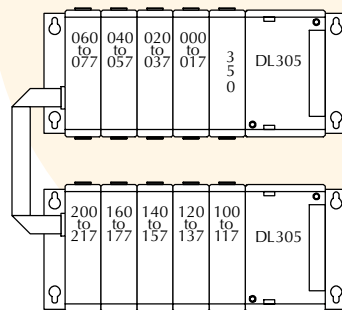
5-slot local  
Total I/O: 64



5-slot local and two 5-Slot expansions  
Total I/O: 224

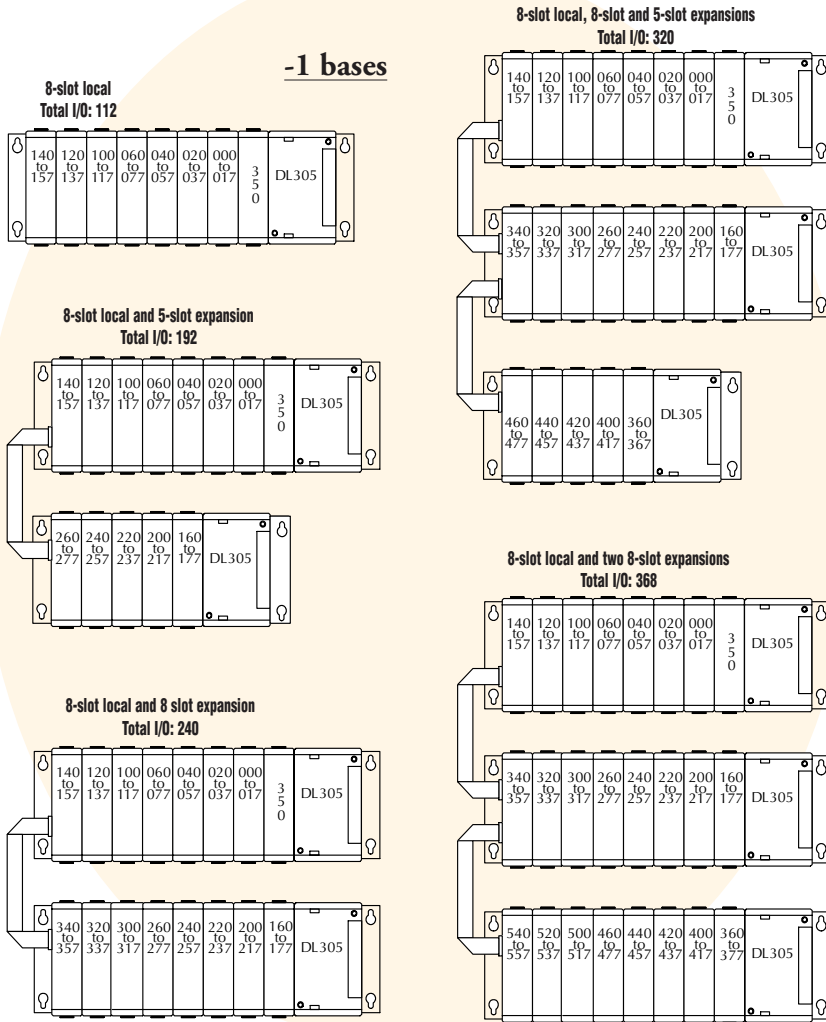


5-slot local and 5-slot expansions  
Total I/O: 144

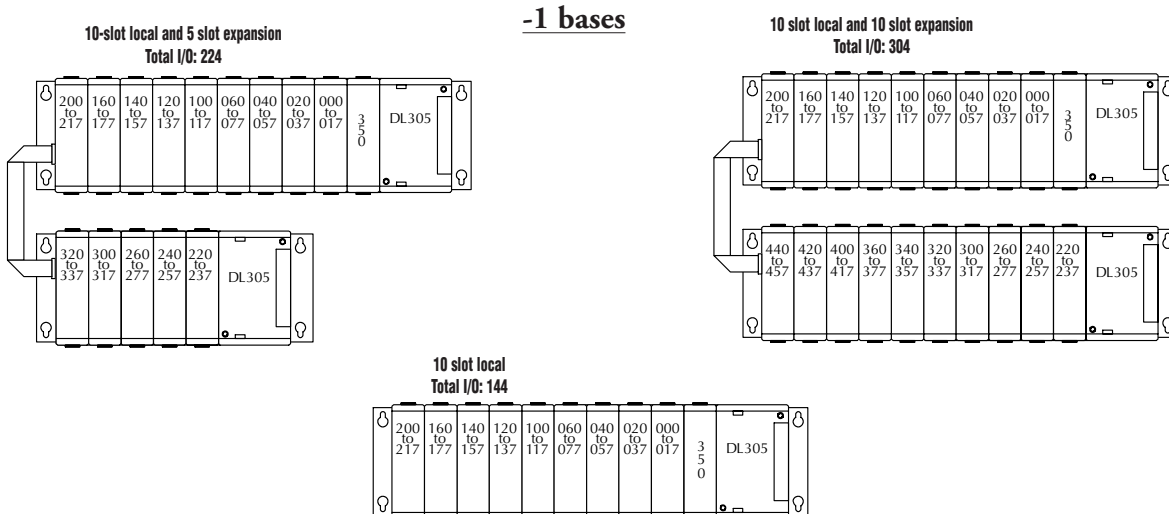


# D3-350 ADDRESSING

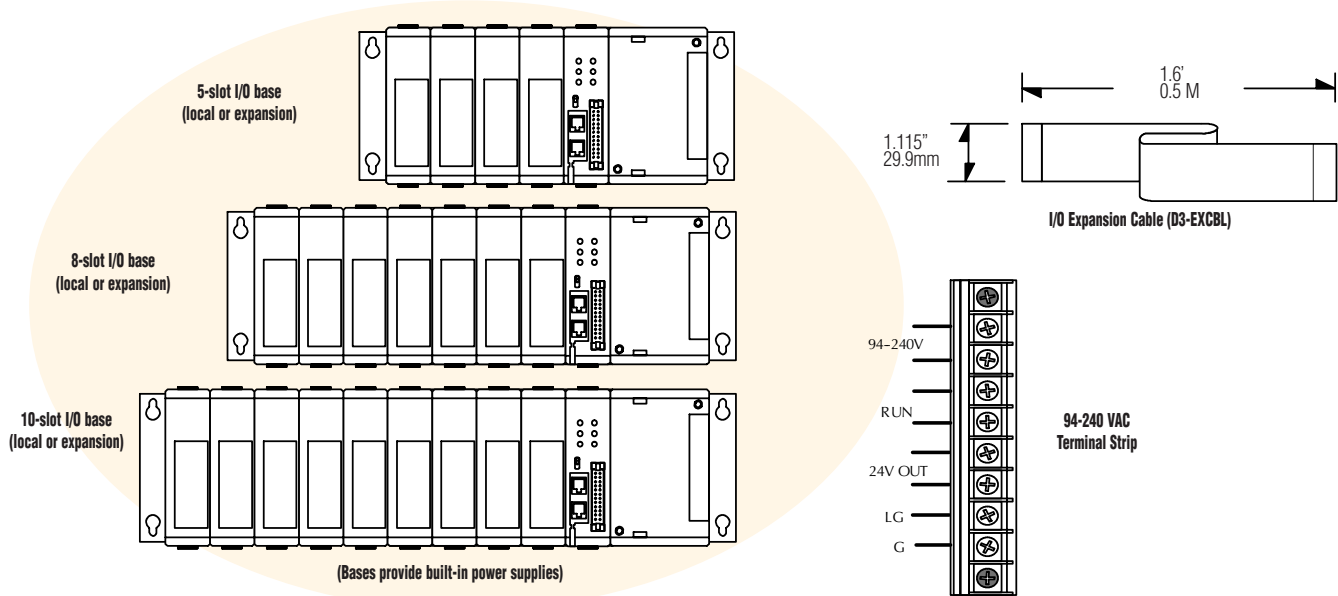
## 8-slot base example configurations



## 10-slot base example configurations



# DL305 BASE SPECIFICATIONS



	D3-05B-1 <--->	D3-05BDC <--->	D3-08B-1 <--->	D3-10B-1 <--->	D3-10BDC <--->
<b>Number of Slots</b>	5	5	8	10	10
<b>Local CPU Base</b>	Yes	Yes	Yes	Yes	Yes
<b>Expansion Base</b>	Yes CPU base and two expansion bases. If CPU base is 5-slot, then the expansion bases must be 5-slot also.	Yes CPU base and two expansion bases. If CPU base is 5-slot, then the expansion bases must be 5-slot also.	Yes (D3-350 only) CPU base and two expansion bases. If CPU base is 8-slot, then the expansion bases must be 8-slot or 5-slot	Yes CPU base and one expansion bases. If CPU base is 10-slot, then the expansion bases must be 10-slot or 5-slot	Yes CPU base and one expansion bases. If CPU base is 10-slot, then the expansion bases must be 10-slot or 5-slot.
<b>Input Voltage Range</b>	85-264VAC 47-63Hz	20.5-30VDC <10% ripple	85-264VAC 47-63Hz	85-264VAC 47-63Hz	20.5-30VDC <10% ripple
<b>Base Power Consumption</b>	85 VA Max	48 Watts	85VA Max	85VA Max	65 Watts
<b>Inrush Current Max.</b>	30A 1ms	30A	30A 1ms	30A 1ms	30A
<b>Dielectric Strength</b>	1500VAC for one minute between terminals of AC P/S, run output, common, 24VDC	1500VAC for one minute between 24VDC input terminals and run output	1500VAC for one minute between terminals of AC P/S, run output, common, 24VDC	2000VAC for one minute between terminals of AC P/S, run output, common, 24VDC	1500VAC for one minute between 24VDC input terminals and run output
<b>Insulation Resistance</b>	>10Mohm at 500VDC	>10Mohm at 500VDC	>10Mohm at 500VDC	>10Mohm at 500VDC	>10Mohm at 500VDC
<b>Power Supply Output (Voltage Ranges and Ripple)</b>	(5VDC) 4.75-5.25V 5% ripple (9VDC) 8.5-10V 5% ripple (24VDC) 20-28V 5% ripple	(5VDC) 4.75-5.25V 5% ripple (9VDC) 8.5-10V 5% ripple (24VDC) 20-28V 5% ripple	(5VDC) 4.75-5.25V 5% ripple (9VDC) 8.5-10V 5% ripple (24VDC) 20-28V 5% ripple	(5VDC) 4.75-5.25V 5% ripple (9VDC) 8.5-10V 5% ripple (24VDC) 20-28V 5% ripple	(5VDC) 4.75-5.25V 5% ripple (9VDC) 8.5-10V 5% ripple (24VDC) 20-28V 5% ripple
<b>5 VDC Current Supplied</b>	.7A	1.4A	1.0A	1.0A	1.4A
<b>9 VDC Current Supplied</b>	2.0A	0.8A	2.0A	2.0A	1.7A
<b>24 VDC Current Supplied</b>	0.5A	0.5A	0.5A	0.5A	0.5A
<b>Auxiliary 24 VDC Output</b>	100mA max	None	100mA max	100mA max	None
<b>Run Relay</b>	250VAC 4A (resistive load)	250VAC 4A (resistive load)	250VAC 4A (resistive load)	250VAC 4A (resistive load)	250VAC, 4A (resistive load)
<b>Fuses</b>	2A (250V) Non-replaceable	4A (250V) User-replaceable D3-FUSE-3 <--->	2A (250V) Non-replaceable	2A (250V) Non-replaceable	4A (250V) User-replaceable D3-FUSE-3 <--->
<b>Dimensions W/H/D</b>	11.42x4.85x4.41 in. (290x123x112 mm)	11.42x4.85x4.41 in. (290x123x112 mm)	15.55x4.85x4.41 in. (395x123x112 mm)	18.3x4.85x4.41 in. (465x123x112 mm)	18.34x4.85x4.41 in. (465x123x112 mm)
<b>Weight</b>	37oz. (1050g)	34oz. (964g)	44oz. (1250g)	51.1oz. (1450g)	50.5oz. (1432g)

# POWER BUDGET

## Managing your power resource

The I/O configuration depends on your choice of I/O modules, bases and I/O location. When determining the types and quantity of I/O modules you will be using, it's important to remember there is a limited amount of power available from the power supply.

The chart on the next page indicates the power supplied and used by each DL305 device. The adjacent chart shows an example of how to calculate the power used by your particular system. These two charts should make it easy for you to determine if the devices you have chosen fit within the power budget of your system configuration.

If the I/O you have chosen exceeds the maximum power available from the power supply, you can resolve the problem by shifting some of the modules to an expansion base.

## Use ZIPLinks to reduce power requirements

If your application requires a lot of relay outputs, consider using the ZipLink AC or DC relay output modules. These modules can switch high current (10A) loads without putting a load on your base power budget. Refer to the Connection Systems section of this desk reference to find out more about ZipLink cables and connector modules.

This logo is placed next to I/O modules that are supported by the ZipLink connection systems. See the I/O module specifications at the end of this section.



**WARNING:** It is extremely important to calculate the power budget correctly. If you exceed the power budget, the system may operate in an unpredictable manner, which may result in a risk of personal injury or equipment damage.

## Example: how to calculate your power usage

The following example shows how to calculate the power budget for the DL305 system. The examples are constructed around a single 5-slot base using the devices shown. It is recommended you construct a similar table for each base in your DL305 system.

- Using a chart similar to the one below, fill in column 2.
- Using the tables on the opposite page, enter the current supplied and used by each device (columns 3, 4, and 5). Devices which fall into the "Other" category (Row D) are devices such as the Handheld Programmer or a Data Communication Unit, which also have power requirements, but do not directly plug into the base.
- Add the current used by the system devices (columns 3, 4, and 5), starting with Slot 1, then

put the total in the row labeled "Maximum Current Required" (Row E).

- Subtract the row labeled "Maximum Current Required" (Row E), from the row labeled "Current Supplied" (Row B). Place the difference in the row labeled "Remaining Current" (Row F).

- If "Maximum Current Required" is greater than "Current Supplied" in columns 3, 4 or 5, the power budget will be exceeded. It will be unsafe to use this configuration and you will need to restructure your I/O configuration.

A	Column 1	Column 2	Column 3	Column 4	Column 5
<b>Base # 0</b>	Device Type	5 VDC (mA)	9VDC (mA)	24V(mA)	
<b>B</b>	<b>Current Supplied</b>				
	<b>5-slot Base</b>	D3-05BDC	1400	800	500
<b>C</b>	<b>Current Required</b>				
	<b>CPU Slot</b>	D3-330	300	50	0
	<b>Slot 0</b>	D3-16NE3	0	130	0
	<b>Slot 1</b>	D3-16NE3	0	130	0
	<b>Slot 2</b>	F3-16TA-1	0	160	0
	<b>Slot 3</b>	F3-16TA-1	0	160	0
<b>D</b>	<b>Other</b>				
	Handheld prog D3-HPP		50	50	0
<b>E</b>	<b>Maximum Current Required</b>		360	680	0
<b>F</b>	<b>Remaining Current</b>		1040	120	500

# DL305 POWER REQUIREMENTS

This section shows the amount of power supplied by the base power supplies and the amount of power used by each DL305 device. Note the base power supplies provide three internal voltages (5 V, 9 V, 24 V). The chart shows how much power from each of these power sources is required for each DL305 device. Use this information when calculating the power budget for your system.

In addition to the three internal power sources, the DL305 bases provide an external power connection. There is 24 VDC available from the 24 VDC output terminals on the bases (except D3-05BDC and D3-10BDC).

The 24 VDC can be used to power external devices or DL305 modules that require external 24 VDC. The power used from this external 24 VDC output reduces the internal system 24 VDC that is available to the modules by an equal amount. When using the 24 VDC output at the base terminal, it is recommended that 100 mA not be exceeded.

Power Consumed				
Device	5V(mA)	9V(mA)	24V(mA)	Ext req.
<b>CPUs</b>				
D3-330	300	50	0	0
D3-340	300	20	0	0
D3-350	500	0	0	0
<b>DC Input Modules</b>				
D3-08ND2	0	10	112	0
D3-16ND2-1	0	25	224	0
D3-16ND2F	0	25	224	0
F3-16ND3F	0	148	68	0
<b>AC Input Modules</b>				
D3-08NA-1	0	10	0	0
D3-08NA-2	0	10	0	0
D3-16NA	0	100	0	0
<b>AC/DC Input Modules</b>				
D3-08NE3	0	10	0	0
D3-16NE3	0	130	0	0
<b>DC Output Modules</b>				
D3-04TD1	0	12	5	0
D3-08TD1	0	20	24	0
D3-08TD2	0	30	0	0
D3-16TD1-1	0	40	96	0
D3-16TD2	0	180	0	0
<b>AC Output Modules</b>				
D3-04TAS	0	12	0	0
F3-08TAS	0	80	0	0
D3-08TA-1	0	160	0	0
D3-08TA-2	0	160	0	0
F3-16TA-2	0	250	0	0
D3-16TA-2	0	400	0	0

Power Supplied				
Device	5V(mA)	9V(mA)	24V(mA)	24 V Auxiliary
D3-08B-1	900	2000	500	Yes
D3-08B-1	900	2000	500	Yes
D3-10B-1	900	2000	500	Yes
D3-05BDC	900	2000	500	Yes
D3-10BDC	900	2000	500	Yes
D3-05B-NR	900	2000	500	Yes
D3-08B-NR	900	2000	500	Yes
D3-10B-NR	900	2000	500	Yes
D3-05BDC-NR	900	2000	500	Yes
D3-08BDC-NR	900	2000	500	Yes
D3-10BDC-NR	900	2000	500	Yes
<b>Power Consumed</b>				
Device	5V(mA)	9V(mA)	24V(mA)	External required
<b>Relay Output Modules</b>				
D3-08TR	0	360	0	0
F3-08TRS-1	0	296	0	0
F3-08TRS-2	0	296	0	0
D3-16TR	0	480	0	0
<b>Analog Temperature and Thermocouple Modules</b>				
F3-04ADS	0	183	50	0
F3-08AD-1	0	45	55	0
F3-08THM-n	0	50	34	0
F3-16AD	0	55	65	0
F3-04DA-1	0	144	108	0
F3-04DAS	0	154	145	0
<b>Communications and Networking</b>				
D3-232 DCU	500	0	0	Optional 5V@500mA
D3-422 DCU	500	0	0	Optional 5V@500mA
FA-UNICON	0	0	0	24V or 5V@ 100mA
D3- DCM	0	300	0	0
<b>Specialty Modules</b>				
D3-08SIM	0	10	112	0
D3-HSC	0	70	0	0
D3-TCSU	40	5	0	0
<b>Programming</b>				
D3-HP	50	50	0	0
D3-HPP	50	50	0	0
D2-HP	200	0	0	0
<b>Specialty CPUs</b>				
F3-OMUX-1	409	0	0	0
F3-OMUX-2	262	0	150	0
F3-PMUX	455	0	0	0
F3-RTU	416	0	0	0

# DIMENSIONS AND INSTALLATION

It is important to understand the installation requirements for your DL305 system. This will help ensure that the DL305 products operate within their environmental and electrical limits.

## Plan for safety

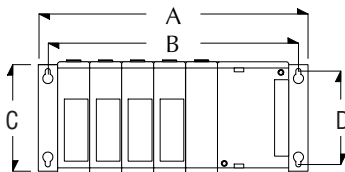
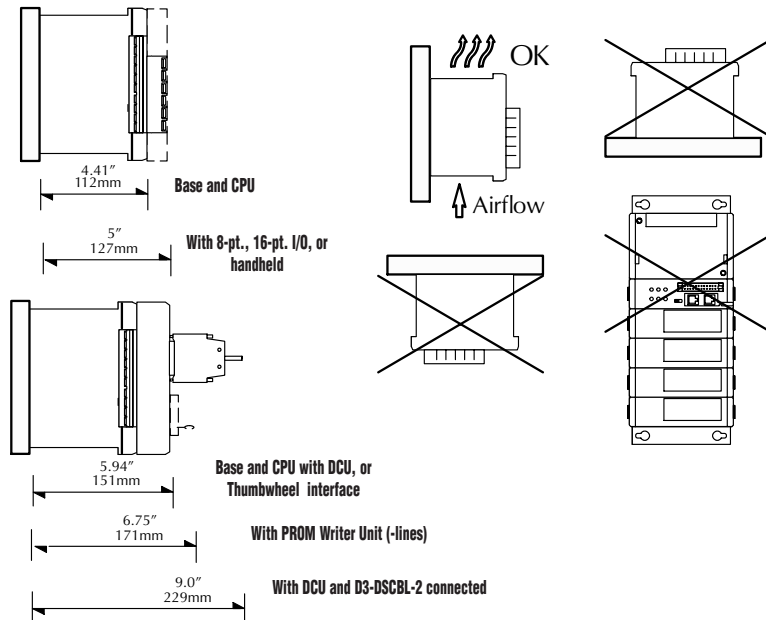
This catalog should never be used as a replacement for the user manual. The user manuals, D3-USER-M and D3-350-M (available for download from our web site), contain important safety information that must be followed. The system installation should comply with all appropriate electrical codes and standards.

## Base dimensions and mounting orientation

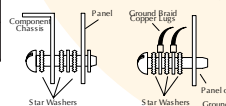
Use the diagrams to the right to make sure the DL305 system can be installed in your application. DL305 bases must be mounted horizontally to ensure proper airflow for cooling purposes. It is important to check these dimensions against the conditions required for your application. For example, it is recommended that you leave 1.5" depth for ease of access and cable clearance. However, your distance may be greater or less. Also, check the installation guidelines for the recommended cabinet clearances.

Specification	Rating
<b>Storage Temperature</b>	-4°F - 158°F (-20°C to 70°C)
<b>Ambient Operating Temperature</b>	32°F - 131°F (0° to 55°C)
<b>Ambient Humidity</b>	30% - 95% relative humidity (non-condensing)
<b>Vibration Resistance</b>	MIL STD 810C, Method 514.2
<b>Shock Resistance</b>	MIL STD810, Method 516.2
<b>Noise Immunity</b>	NEMA (ICS3-304)

DL305 mounting depth



Base	Price	A	B	C	D				
<b>D3-05B-1</b>	<-->	11.41"	290mm	10.63"	270mm	4.84"	123mm	3.54"	90mm
<b>D3-08B-1</b>	<-->	15.55"	395mm	14.76"	375mm	4.84"	123mm	3.54"	90mm
<b>D3-10B-1</b>	<-->	18.30"	465mm	17.51"	445mm	4.84"	123mm	3.54"	90mm



See the Enclosures section in this desk reference for an enclosure that may be suitable for your application



# DATA COMMUNICATIONS MODULE

## D3-DCM

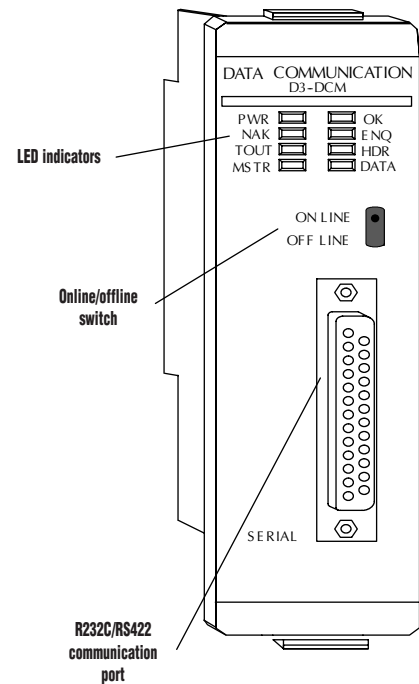
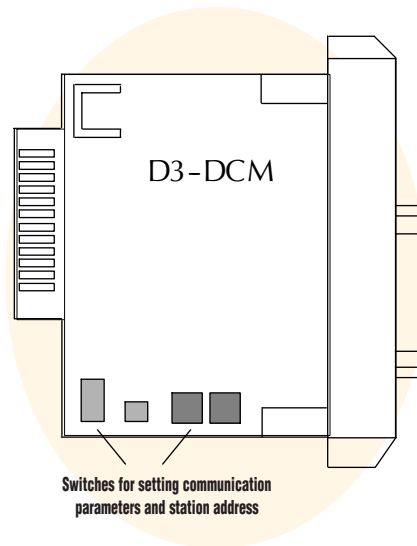


The DL305 Data Communications Module (DCM) is a general purpose communications interface for the DL305 family of PLCs. This module only works with the D3-350 CPUs. It will not work with the D3-330 or D3-340 CPUs or in DC-powered bases. You must use the “-1” type base. The DCM module is primarily used for three purposes:

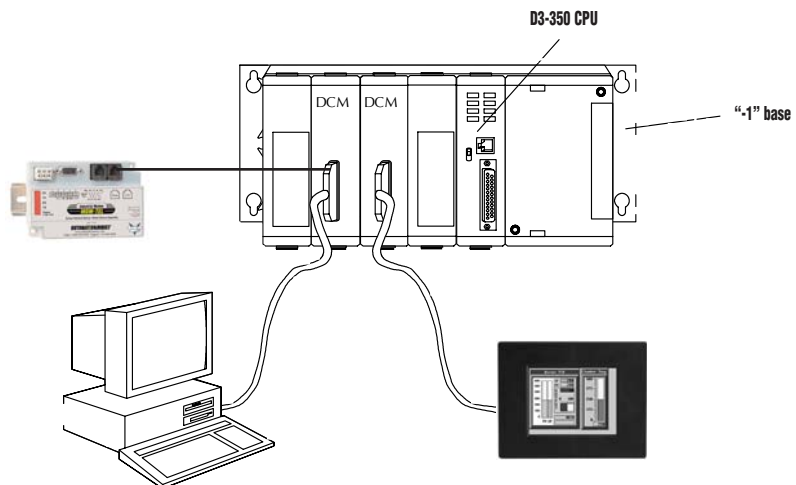
- Extra general purpose communications port to connect a personal computer, operator interface, etc.
- Network interface to a DirectNET network
- Network interface to a MODBUS RTU protocol

The D3-350 CPU offers a built-in communication port. However, if more communication ports are needed, they can easily be added by installing Data Communication Modules. Any device that can be connected to the communication port of a D3-350 CPU can be connected to the DCM. However, make sure the device has a DL305-compatible driver. This allows additional connections of devices, such as operator interfaces, personal computers, etc. Since the DCM does not require any programming, you can set the DCM communication parameters, connect the cables, and start transferring data.

Specifications	
<b>Module Type</b>	Intelligent
<b>Modules per CPU</b>	3 maximum, slot 0 to 7, cannot use slot 8
<b>CPUs Supporting the DCM</b>	D3-350 (firmware V1.08C or later)
<b>Communications</b>	<ul style="list-style-type: none"> <li>• RS232C/422 signal levels <b>DirectNET™</b></li> <li>• K-sequence or MODBUS RTU slave protocol</li> <li>• Baud rate selectable from 300 to 38.4K baud</li> <li>• Odd or NO parity. HEX or ASCII mode</li> </ul>
<b>Recommended Cable</b>	Belden 9729 or equivalent
<b>Field Wiring Connector</b>	25-pin D-shell connector
<b>Internal Power Consumption</b>	300mA maximum at 9VDC, (supplied by base power supply)
<b>Operating Environment</b>	0C° to 60°C (32°F to 140°F), 5% to 95% humidity (non-condensing)
<b>Manufacturer</b>	Koyo Electronics



Connect the DCM to our MDM-TEL serial modem (see the Communication Products section of this desk reference for details on the modem)



# DATA COMMUNICATIONS MODULE

## DirectNET network interface

The DCM can be used as a network interface for applications requiring data to be shared between PLCs, or between PLCs and an intelligent device such as a host computer. The DCM connects easily to *DirectNET*. This network allows you to upload or download virtually any type of system data including Timer/Counter data, I/O information, and V-memory information from most *DirectLOGIC* or compatible PLCs. The DCM allows the D3-350 CPU to function as a network master or as a network slave.

## Low-cost MODBUS interface

The DCM can be used as a slave station interface to connect your D3-350 system to the MODBUS network using the MODBUS RTU protocol. The host system must be capable of issuing the MODBUS commands to read or write the appropriate data. Since the D3-350 CPU can act as a MODBUS master, it is now very easy to implement an entire D3-350 control scheme using the MODBUS RTU protocol.

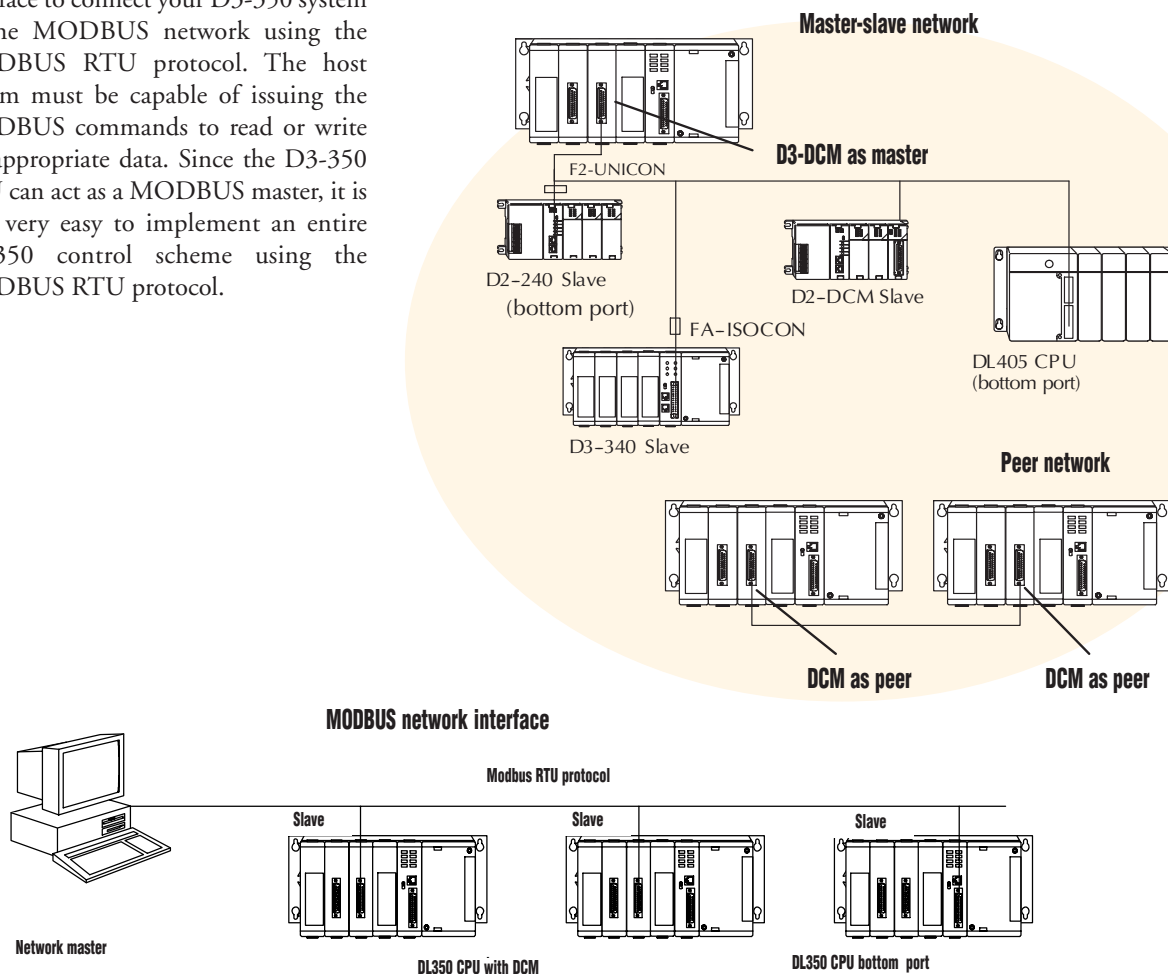
## Network master

The DCM allows the D3-350 to serve as a master of a *DirectNET* Network. The DCM takes communication requests issued from the CPU's RLL program (the network part of the program can be very simple, as few as 7 words) and automatically converts these requests into network commands to read data from or write data to a network slave station. This capability also allows a simple peer-to-peer configuration of two D3-350 systems, each with a DCM. In this scenario, either station can initiate the request for data.

*Note: The F1-130 CPUs and the D2-230 do not support DirectNet.*

## Network slave

The D3-350 CPU has built-in ports that support the *DirectNET* protocol. If these ports are occupied, a DCM can be added to provide an additional network slave port. In this case, the DCM "listens" to the network for any messages containing the DCM's address. The DCM deciphers the network commands, carries out the request to read or write data and sends confirmation and/or information to the master station. Since the DCM does not require any programming, you can set the DCM communication parameters, connect the cables and start transferring data.



# DATA COMMUNICATIONS UNITS

## D3-232-DCU <---> D3-422-DCU <--->

The DL305 Data Communications Unit (DCU) is a general purpose communications interface for the DL305 family. This unit is used with the D3-330 CPU for the following reasons:

- As a general purpose communications port to connect a personal computer, operator interface, etc.
- Network interface to a DirectNET network.
- The DCU is not necessary for the D3-340 or D3-350 CPUs since they have built-in communication ports.

### Mode 1: general purpose port

You can use the DCU as a general purpose communication port to connect your DL305 to various devices such as an operator interface or personal computer. The DCU does not require any programming. You can simply set the DCU communication parameters, connect the appropriate RS232C or RS422 communication cables, and start programming or transferring data. Typically this application would use the RS232C version of the DCU (D3-232-DCU).

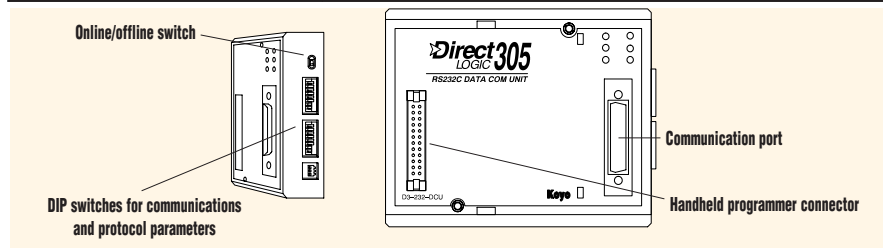
## FA-UNICON <--->



The FA-UNICON Universal Converter provides an inexpensive way to convert D3-422-DCU RS-422 signals into RS-232C signals for connection to a PC. This kit contains the converter itself and several specialty connectors and cables that make it easy to use and install. The converter requires an external power source for operation.

The FA-UNICON can also be used with GE IC610CCM105 and TI 305-02DM RS-422 DCU units.

Specifications	
<b>Unit Type</b>	Intelligent
<b>Units per CPU</b>	One, direct connect to CPU
<b>Communications</b>	RS232C with D3-232-DCU RS422 with D3-422-DCU DirectNET™. Baud rate selectable from 300 to 19.2K baud. Odd or No parity. HEX or ASCII mode.
<b>Field Wiring Connector</b>	25-Pin D-shell connector
<b>Internal Power Consumption</b>	500mA from 5VDC maximum. Supplied by base power supply or external supply.
<b>Operating Environment</b>	0°C to 60°C (32°F to 140°F), 5% to 95% humidity (non-condensing)
<b>Manufacturer</b>	Koyo Electronics

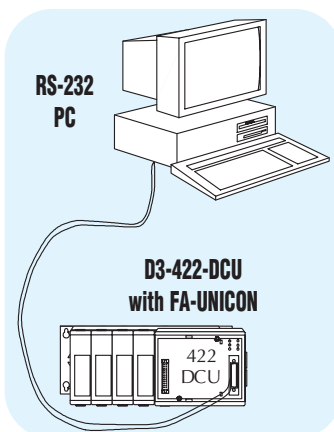
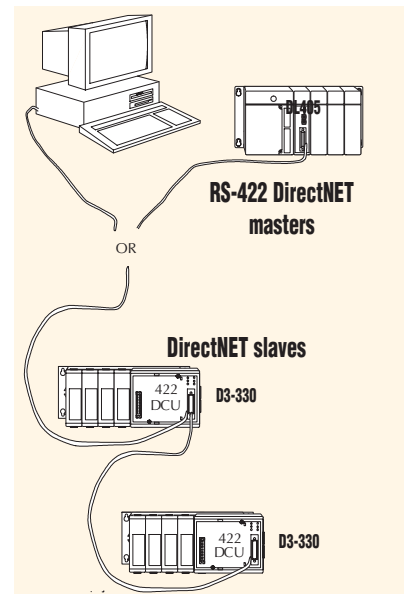


### Mode 2: DirectNET network interface

The DCU can be used as a network interface for applications requiring data to be shared between PLCs, or between PLCs and an intelligent device such as a host computer. The DCU supports DirectNET protocol (often called HostLink or CCM). Since all of our product families support this protocol, you can easily build a simple network that allows you to upload or download virtually any type of system data,

including Timer/Counter data, I/O information, and V-memory information. Typically this application would use the RS422 version of the DCU (D3-422-DCU).

#### RS422 DirectNET Network



The FA-UNICON contains the following items:

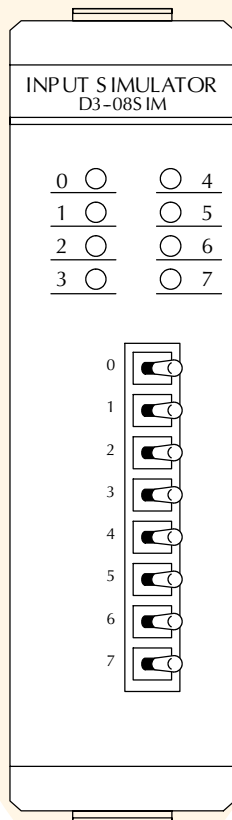
- RS-232/422 converter
- 25-pin male-to-male gender changer
- 25-pin male to RJ12 6P6C connector
- 3' cable, RJ12 6-pin plug to RJ11 4-pin plug
- 7' cable with RJ12 6-pin plug to RJ12 6-pin
- DB9 Female to RJ12 6P6C connector

#### FA-UNICON Specifications

- Supply voltage: 22-26 VDC or 5VDC
- No load current: 65mA
- Max. current: 100mA
- Operating temp: 45°C (113°F) for 24V supply, 60°C (140°F) for 5V supply

# SIMULATOR MODULE

D3-08SIM Input Simulator <--->	
<b>Inputs per Module</b>	8
<b>Base Power Required</b>	10mA @ 9VDC 112mA max @ 24VDC
<b>OFF to ON Response</b>	4-15ms
<b>ON to OFF Response</b>	4-15ms
<b>Terminal Type</b>	None
<b>Status Indicators</b>	Switch side
<b>Weight</b>	3.0oz. (85g)

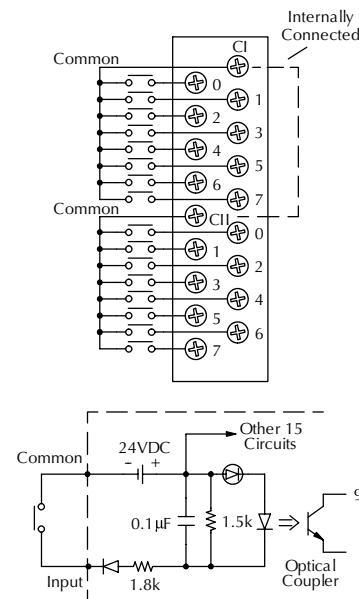
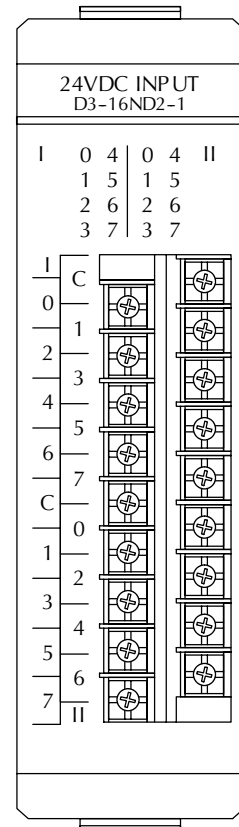
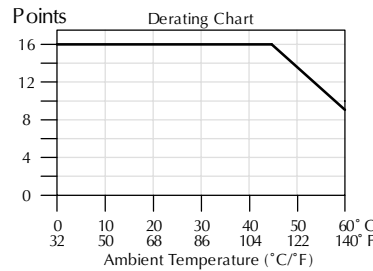
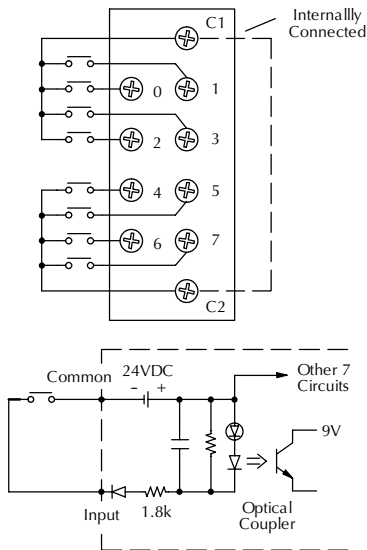
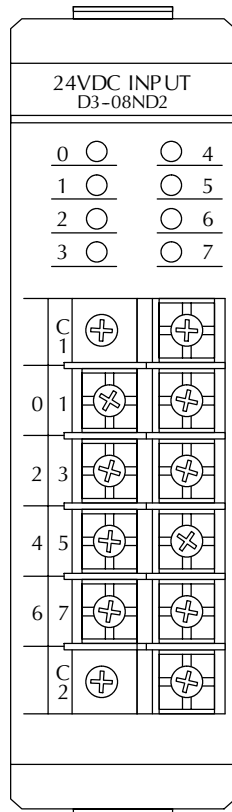
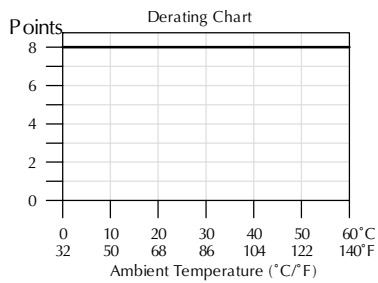


# DC INPUT MODULES

D3-08ND2 DC Input <--->	
<b>Inputs per Module</b>	8 (current sourcing)
<b>Commons per Module</b>	2 (internally connected)
<b>Input Voltage Range</b>	18-36VDC
<b>Input Voltage</b>	Internally supplied
<b>Peak Voltage</b>	40VDC
<b>AC Frequency</b>	N/A
<b>ON Voltage Level</b>	>18V
<b>OFF Voltage Level</b>	<3V
<b>Input Impedance</b>	1.8Kohm
<b>Input Current</b>	12mA max
<b>Minimum ON Current</b>	7mA
<b>Maximum OFF Current</b>	3mA
<b>Base Power Required</b>	9V 10mA max. 24V 14mA/ON pt. (112mA max)
<b>OFF to ON Response</b>	4-15ms
<b>ON to OFF Response</b>	4-15ms
<b>Terminal Types</b>	Non-removable
<b>Status Indicators</b>	Field side
<b>Weight</b>	4.2oz. (120g)

D3-16ND2-1 DC Input <--->	
<b>Inputs per Module</b>	16 (current sourcing)
<b>Commons per Module</b>	2 (internally connected)
<b>Input Voltage Range</b>	18-36VDC
<b>Input Voltage</b>	Internally supplied
<b>Peak Voltage</b>	36VDC
<b>AC Frequency</b>	N/A
<b>ON Voltage Level</b>	>19V
<b>OFF Voltage Level</b>	<3V
<b>Input Impedance</b>	1.8Kohm
<b>Input Current</b>	20mA Max
<b>Minimum ON Current</b>	5mA
<b>Maximum OFF Current</b>	1mA
<b>Base Power Required</b>	9V 25mA Max 24V 14mA/ON pt. (224mA max)
<b>OFF to ON Response</b>	3-15ms
<b>ON to OFF Response</b>	4-15ms
<b>Terminal Types</b>	Removable
<b>Status Indicators</b>	Field side
<b>Weight</b>	6.3oz. (180g)

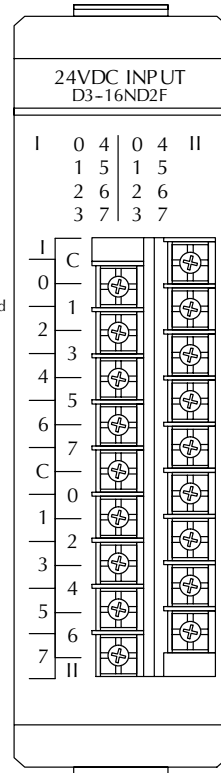
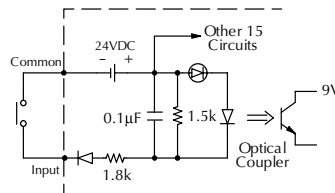
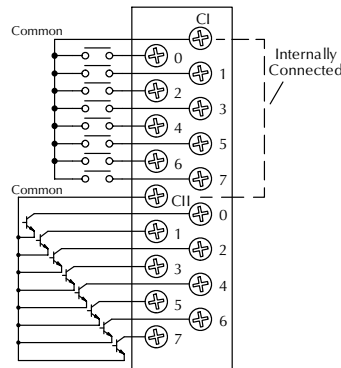
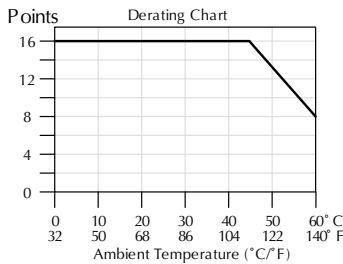
See the Connection Systems section in this desk reference for part numbers of ZIPLinks cables and terminal blocks compatible with this module.



# DC INPUT MODULES

D3-16ND2F Fast Response Input <---->	
<b>Inputs per Module</b>	16 (current sourcing)
<b>Commons per Module</b>	2 (internally connected)
<b>Input Voltage Range</b>	18-36VDC
<b>Input Voltage</b>	Internally supplied
<b>Peak Voltage</b>	36VDC
<b>AC Frequency</b>	N/A
<b>ON Voltage Level</b>	>19V
<b>OFF Voltage Level</b>	<13V
<b>Input Impedance</b>	1.8Kohm
<b>Input Current</b>	20mA max
<b>Minimum ON Current</b>	5mA
<b>Maximum OFF Current</b>	1mA
<b>Base Power Required</b>	9V 25mA max 24V 14mA/ON pt. (224mA max)
<b>OFF to ON Response</b>	0.8ms
<b>ON to OFF Response</b>	0.8ms
<b>Terminal Types</b>	Removable
<b>Status Indicators</b>	Field side
<b>Weight</b>	6.3oz. (180g)

See the Connection Systems section in this desk reference for part numbers of ZIPLinks cables and terminal blocks compatible with this module.



# DC INPUT MODULES

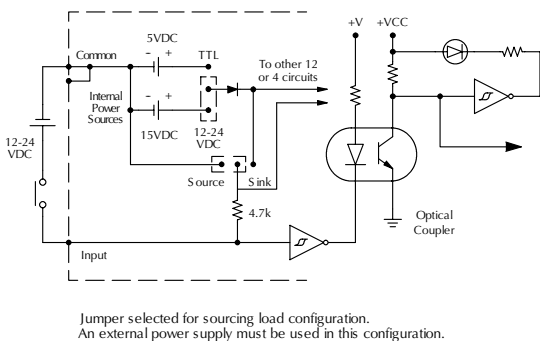
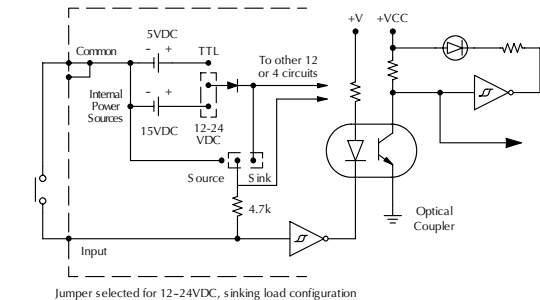
F3-16ND3F DC Fast Response Input <---->	
<b>Inputs per Module</b>	16 sink/source (jumper selectable sink/source)*
<b>Commons per Module</b>	2 (internally connected)
<b>Input Voltage Range</b>	5VDC TTL and CMOS, 12-24VDC (jumper selectable)*
<b>Input Voltage Supplied</b>	Internal (used with sinking loads) External (used with sourcing loads)
<b>Peak Voltage</b>	100VDC (35VDC Continuous)
<b>AC Frequency</b>	N/A
<b>ON Voltage Level</b>	3.5-5VDC @ 5VDC 10-24VDC @ 12-24VDC
<b>OFF Voltage Level</b>	0-1.5VDC @ 5VDC 0-4VDC @ 12-24VDC
<b>Base Power Required</b>	9V 148mA max 24V 69mA max
<b>Input Current</b>	1mA @ 5VDC 3mA @ 12-24VDC
<b>Input Impedance</b>	4.7K
<b>OFF to ON Response</b>	1ms
<b>ON to OFF Response</b>	1ms
<b>Maximum Input Rate</b>	500Hz
<b>Minimum ON Current</b>	0.4mA @ 5VDC 0.9mA @ 12-24VDC
<b>Maximum OFF Current</b>	0.8mA @ 5VDC 2.2mA @ 12-24VDC
<b>Terminal Type</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	5.4oz. (153g)

## Selection of operating mode

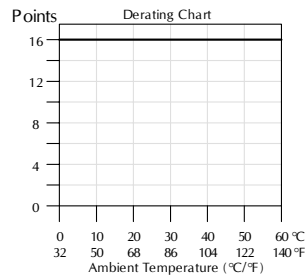
The DC power is provided by the rack power supply to sense the state of the inputs when jumpers are installed for sinking type signals. Sinking type inputs are turned ON by switching the input circuit to common. Source type input signals assume the ON state until the input device provides the voltage to turn the input OFF.

The mode of operation, either 5 VDC or 12-24 VDC sink or source, for each group of circuits is determined by the position of jumper plugs on pins that are located on the bottom edge of the circuit board. There are four sets of pins (3 pins in each set), with two sets for each group of inputs. The first two sets of pins are used to configure the first 12 inputs (e.g. 0 to 7 and 100 to 103) and are labeled 12 CIRCUITS. Above the first set of pins are the labels 12/24 V and 5 V. Above the second set of pins are the labels SINK and SRC (source). To select an operating mode for the first 12 circuits, place a jumper on the two pins nearest the appropriate labels. For example, to select 24 VDC Sink input operation for the first 12 inputs, place a jumper on the two pins labeled 12/24 V and on the two pins labeled SINK. The last two sets of pins are used to configure the last 4 inputs (eg. 104 to 107) and are labeled four CIRCUITS. The operating mode selected for the last group of four inputs can be different than the mode chosen for the first group of 12 inputs. Correct module operation required that each set of three pins have a jumper installed (four jumpers total).

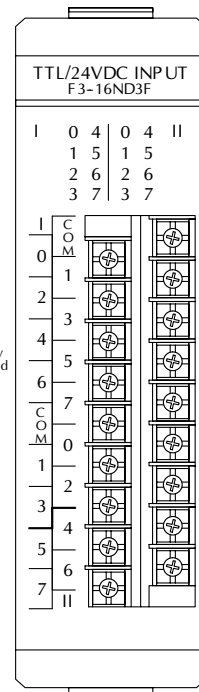
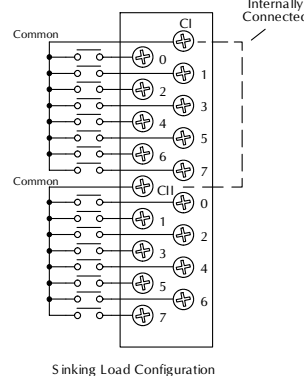
**NOTE:** When a group of inputs is used with TTL logic, select the SINK operating mode for that group. "Standard" TTL can sink several milliamps but can source less than 1mA.



See the Connection Systems section in this desk reference for part numbers of ZIPLinks cables and terminal blocks compatible with this module.



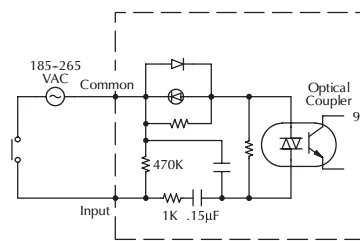
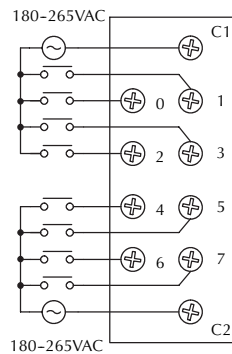
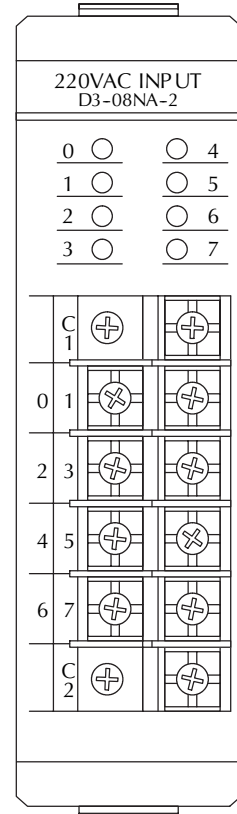
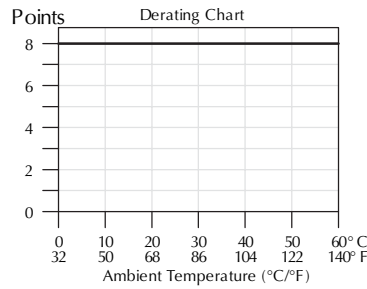
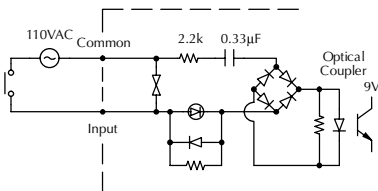
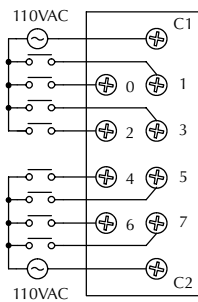
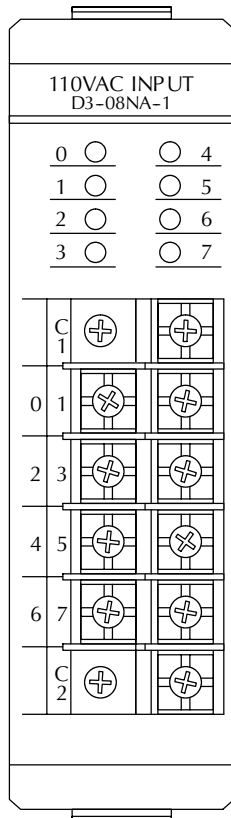
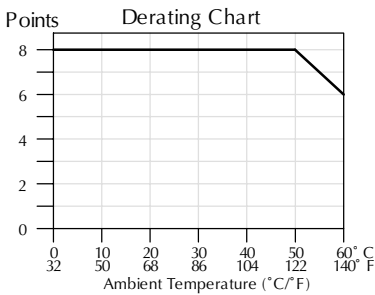
\* 12 Inputs are jumper selectable for 5VDC/12-24VDC and Sink Load/Source Load  
4 Inputs are jumper selectable for 5VDC/12-24VDC and Sink Load/Source Load



# AC INPUT MODULES

D3-08NA-1 AC Input <--->	
<b>Inputs per Module</b>	8
<b>Commons per Module</b>	2 (isolated)
<b>Input Voltage Range</b>	85-132VAC
<b>Input Voltage Supply</b>	External
<b>Peak Voltage</b>	132VAC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Level</b>	>80VAC
<b>OFF Voltage Level</b>	<20VAC
<b>Input Impedance</b>	10 K ohm
<b>Input Current</b>	15 mA @ 50Hz 18 mA @ 60Hz
<b>Minimum ON Current</b>	8mA
<b>Maximum OFF Current</b>	2mA
<b>Base Power Required</b>	9V 10mA max 24V N/A
<b>OFF to ON Response</b>	10-30ms
<b>ON to OFF Response</b>	10-60ms
<b>Terminal Types</b>	Non-removable
<b>Status Indicators</b>	Field side
<b>Weight</b>	5oz. (140g)

D3-08NA-2 AC Input <--->	
<b>Inputs per Module</b>	8
<b>Commons per Module</b>	2 (isolated)
<b>Input Voltage Range</b>	180-265VAC
<b>Input Voltage Supply</b>	External
<b>Peak Voltage</b>	265 VAC
<b>AC Frequency</b>	50-60Hz
<b>ON Voltage Level</b>	>180VAC
<b>OFF Voltage Level</b>	<40VAC
<b>Input Impedance</b>	18Kohm
<b>Input Current</b>	13mA @ 50Hz 18mA @ 60Hz
<b>Minimum ON Current</b>	10mA
<b>Maximum OFF Current</b>	2mA
<b>Base Power Required</b>	9V 10mA max 24V N/A
<b>OFF to ON Response</b>	5-50ms
<b>ON to OFF Response</b>	5-60ms
<b>Terminal Types</b>	Non-removable
<b>Status Indicators</b>	Field side
<b>Weight</b>	5oz. (140g)





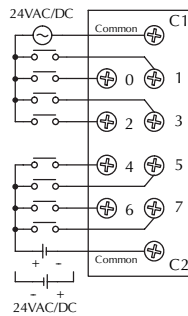
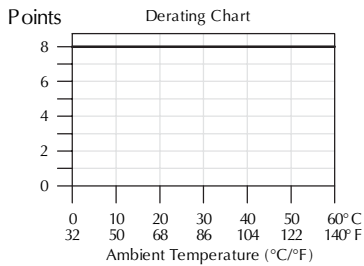
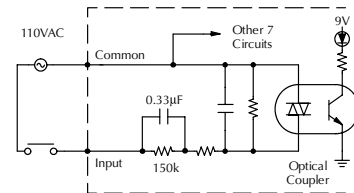
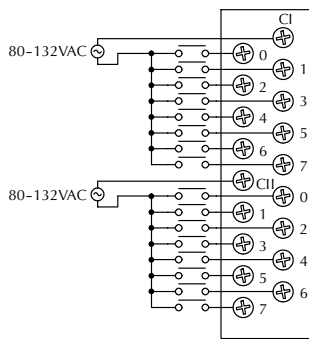
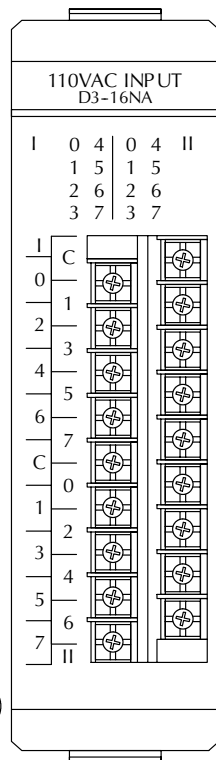
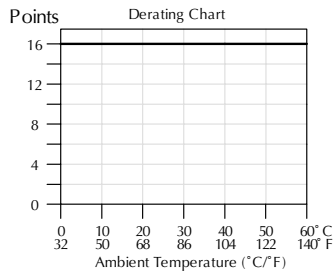
# AC INPUT MODULES

D3-16NA AC Input <--->	
<b>Inputs per Module</b>	16
<b>Commons per Module</b>	2 (isolated)
<b>Input Voltage Range</b>	80-132VAC
<b>Input Voltage Supply</b>	External
<b>Peak Voltage</b>	132VAC
<b>AC Frequency</b>	50-60Hz
<b>ON Voltage Level</b>	>80 VAC
<b>OFF Voltage Level</b>	<15 VAC
<b>Input Impedance</b>	8 K ohm
<b>Input Current</b>	16mA @ 60Hz 24mA @ 60 Hz
<b>Minimum ON Current</b>	8mA
<b>Maximum OFF Current</b>	1.5mA
<b>Base Power Required*</b>	9V 6.25mA max/ON pt 100 mA max
<b>OFF to ON Response</b>	5-50ms
<b>ON to OFF Response</b>	5-60ms
<b>Terminal Types</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	6.4oz. (189g)

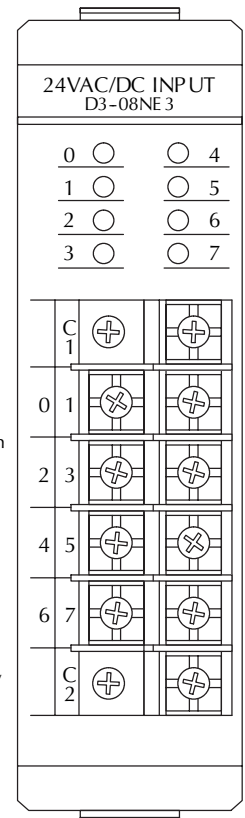
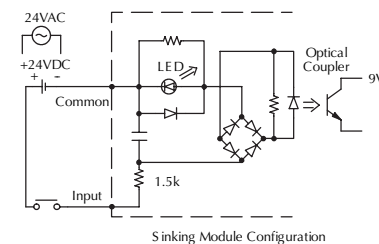
\*9V typical values are 4mA/ON pt., 64mA total

D3-08NE3 AC/DC Input <--->	
<b>Inputs per Module</b>	8 (sink/source)
<b>Commons per Module</b>	2 (isolated)
<b>Input Voltage Range</b>	20-28VAC/VDC
<b>Input Voltage</b>	External
<b>Peak Voltage</b>	28VAC/VDC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Level</b>	>20V
<b>OFF Voltage Level</b>	<6V
<b>Input Impedance</b>	1.5Kohm
<b>Input Current</b>	19mA max
<b>Minimum ON Current</b>	10mA
<b>Maximum OFF Current</b>	2mA
<b>Base Power Required</b>	9V 10mA max 24V N/A
<b>OFF to ON Response</b>	AC: 5-50ms DC: 6-30ms
<b>ON to OFF Response</b>	AC/DC: 5-60ms
<b>Terminal Type</b>	Non-removable
<b>Status Indicators</b>	Field side
<b>Weight</b>	4.2oz. (120g)

See the Connection Systems section in this desk reference for part numbers of ZIPLinks cables and terminal blocks compatible with this module.

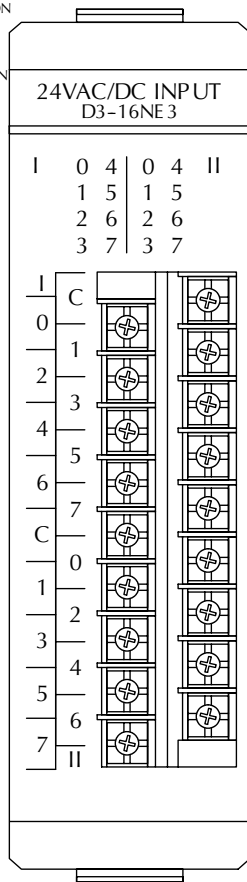
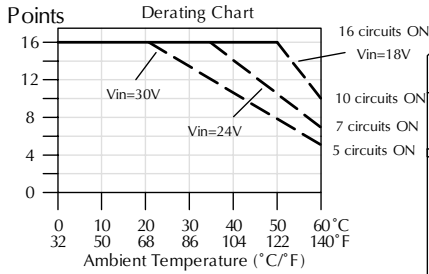


NOTE: This module can be wired in a sourcing configuration and it will be operational except there will be no module LED indication for each input.

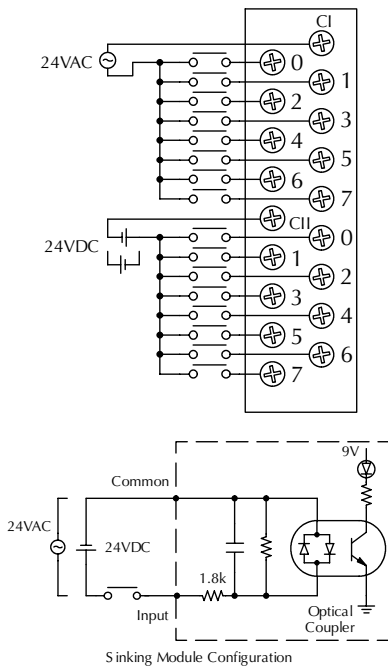


# AC/DC INPUT MODULES

D3-16NE3 AC/DC Input <--->	
<b>Inputs per Module</b>	16 (sink/source)
<b>Commons per Module</b>	2 (isolated)
<b>Input Voltage Range</b>	14-30VAC/VDC
<b>Input Voltage</b>	External
<b>Peak Voltage</b>	30VAC/VDC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Level</b>	>14V
<b>OFF Voltage Level</b>	<3V
<b>Input Impedance</b>	1.8Kohm
<b>Input Current</b>	16mA Max
<b>Minimum ON Current</b>	7mA
<b>Maximum OFF Current</b>	2mA
<b>Base Power Required</b>	9V 2.5mA+4.5mA/ON pt. (130mA max) 24V N/A
<b>OFF to ON Response</b>	AC 5-30ms DC 5-25 ms
<b>ON to OFF Response</b>	AC 5-30ms DC 5-25 ms
<b>Terminal Type</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	6oz. (170g)



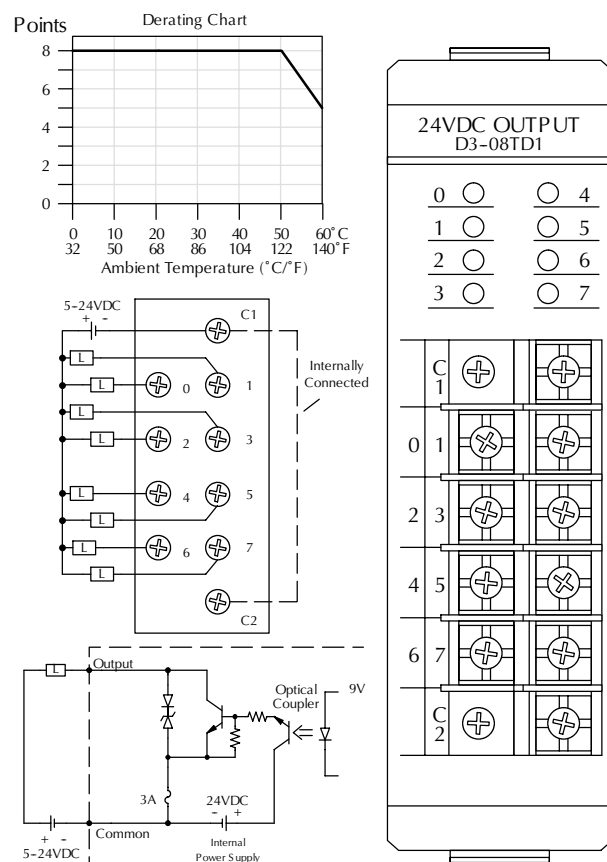
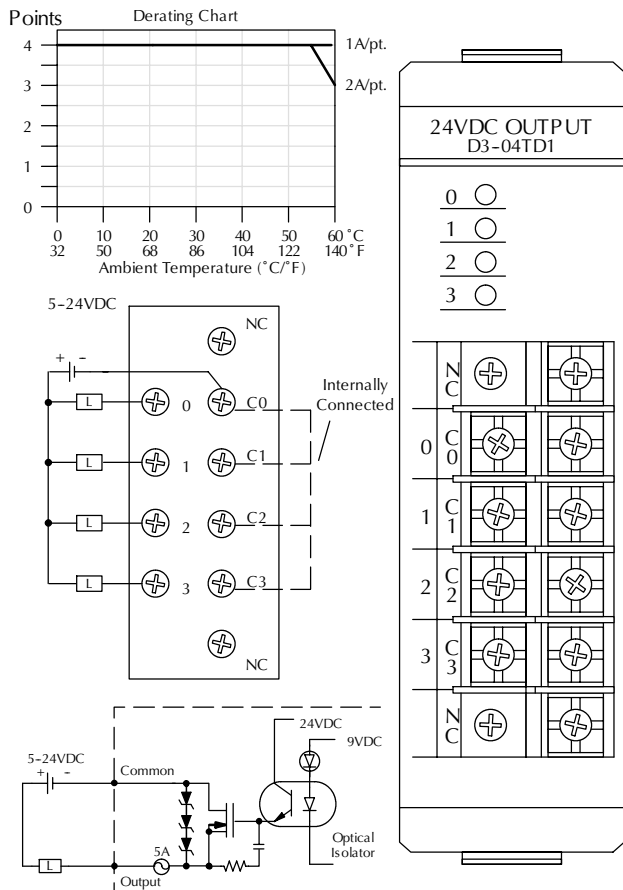
See the Connection Systems section in this desk reference for part numbers of ZIPLinks cables and terminal blocks compatible with this module.



# DC OUTPUT MODULES

D3-04TD1 DC Output <--->	
<b>Outputs per Module</b>	4 (current sinking)
<b>Commons per Module</b>	4 (internally connected)
<b>Operating Voltage</b>	5-24VDC
<b>Output Type</b>	Field effect transistor
<b>Peak Voltage</b>	45VDC
<b>AC Frequency</b>	N/A
<b>ON Voltage Drop</b>	0.9V @ 6A
<b>Max. Current</b>	2A/point 6A/module
<b>Max Leakage Current</b>	0.4mA @ 40VDC
<b>Max Inrush Current</b>	6A/100ms
<b>Minimum Load</b>	5mA
<b>Base Power Required</b>	9V 12mA/pt (48mA Max) 24V 5mA
<b>OFF to ON Response</b>	0.1ms
<b>ON to OFF Response</b>	0.1ms
<b>Terminal Type</b>	Non-removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	4.2oz. (120g)
<b>Fuses</b>	4 (one 5A per common) user replaceable

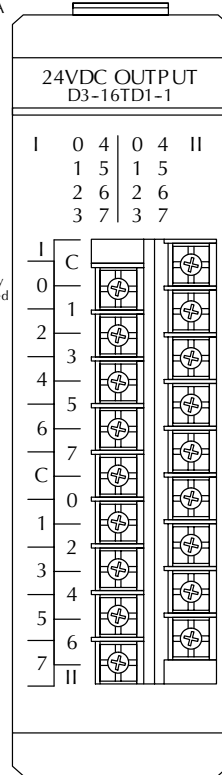
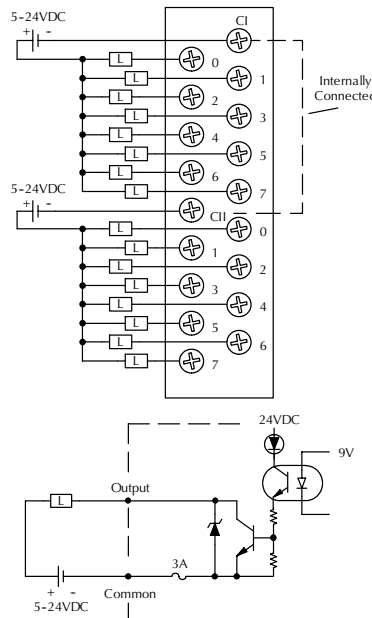
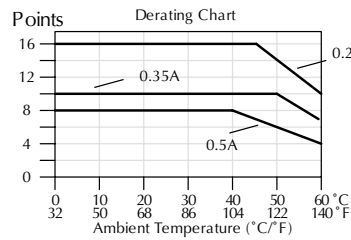
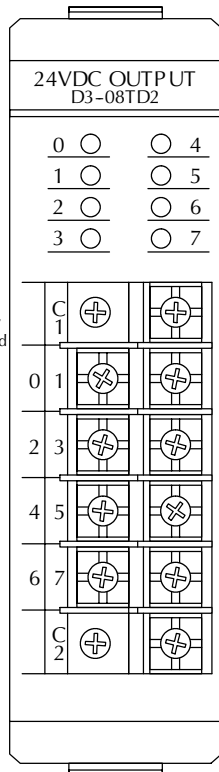
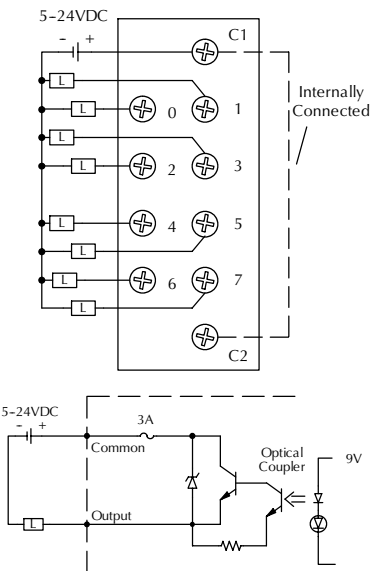
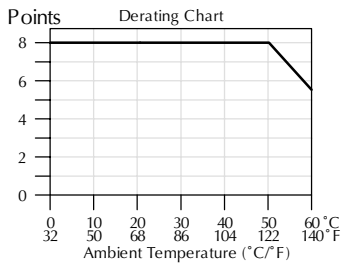
D3-08TD1 DC Output <--->	
<b>Outputs per Module</b>	8 (current sinking)
<b>Commons per Module</b>	2 (internally connected)
<b>Operating Voltage</b>	5-24VDC
<b>Output Type</b>	NPN (open collector)
<b>Peak Voltage</b>	45VDC
<b>AC Frequency</b>	N/A
<b>ON Voltage Drop</b>	0.8V @ 0.5A
<b>Max. Current</b>	0.5A/point 1.8/common
<b>Max Leakage Current</b>	0.1mA @ 40VDC
<b>Max Inrush Current</b>	3A/20ms 1A/100ms
<b>Minimum Load</b>	1mA
<b>Base Power Required</b>	9V 20mA max 24V 3mA/pt (24mA Max)
<b>OFF to ON Response</b>	0.1ms
<b>ON to OFF Response</b>	0.1ms
<b>Terminal Type</b>	Non removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	4.2oz. (120g)
<b>Fuses</b>	2 (one 3A per common) non-replaceable



# DC OUTPUT MODULES

D3-08TD2 DC Output <--->	
<b>Outputs per Module</b>	8 (current sourcing)
<b>Commons per Module</b>	2 (internally connected)
<b>Operating Voltage</b>	5-24VDC
<b>Output Type</b>	NPN transistor (emitter follower)
<b>Peak Voltage</b>	40VDC
<b>AC Frequency</b>	N/A
<b>ON Voltage Drop</b>	1V @ 0.5A
<b>Max. Current</b>	0.5A/point 1.8A/common
<b>Max Leakage Current</b>	0.1mA @ 24VDC
<b>Max Inrush Current</b>	3A/20ms 1A/100ms
<b>Minimum Load</b>	1mA
<b>Base Power Required</b>	9V 30mA max 24V N/A
<b>OFF to ON Response</b>	0.1ms
<b>ON to OFF Response</b>	0.1ms
<b>Terminal Type</b>	Non-removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	4.2oz. (120 g)
<b>Fuses</b>	2 (one 3A per common) non-replaceable

D3-16TD1-1 DC Output <--->	
<b>Outputs per Module</b>	16 (current sinking)
<b>Commons per Module</b>	2 (internally connected)
<b>Operating Voltage</b>	5-24VDC
<b>Output Type</b>	NPN transistor (open collector)
<b>Peak Voltage</b>	45VDC
<b>AC Frequency</b>	N/A
<b>ON Voltage Drop</b>	2V @ 0.5A
<b>Max. Current</b>	0.5A/point 2A/common
<b>Max Leakage Current</b>	0.1mA @ 40VDC
<b>Max Inrush Current</b>	3A/20ms 1A/100ms
<b>Minimum Load</b>	1mA
<b>Base Power Required</b>	9V (40mA max) 3mA+2.3mA/ON pt. 24V 6mA/ON pt. (96mA max)
<b>OFF to ON Response</b>	0.1ms
<b>ON to OFF Response</b>	0.1ms
<b>Terminal Type</b>	Removable
<b>Status Indicator</b>	Logic side
<b>Weight</b>	5.6oz. (160g)
<b>Fuses</b>	2 (one 3A per common) non-replaceable



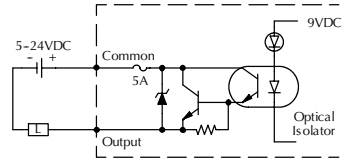
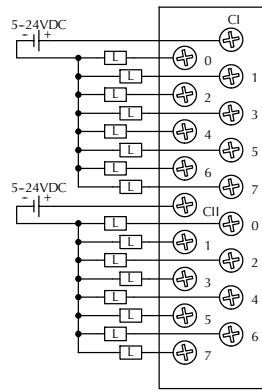
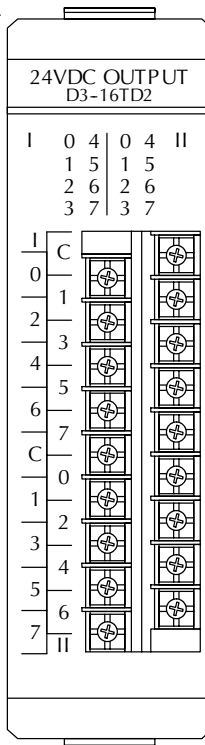
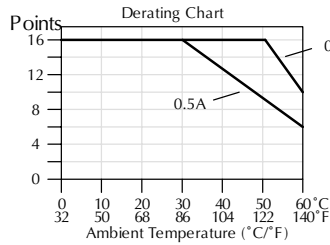
See the Connection Systems section in this desk reference for part numbers of ZIPLinks cables and terminal blocks compatible with this module.



# DC OUTPUT MODULES

D3-16TD2 DC Output <--->	
<b>Outputs per Module</b>	16 (current sourcing)
<b>Commons per Module</b>	2 (isolated)
<b>Operating Voltage</b>	5-24VDC
<b>Output Type</b>	NPN transistor (emitter follower)
<b>Peak Voltage</b>	40VDC
<b>AC Frequency</b>	N/A
<b>ON Voltage Drop</b>	1.5V @ 0.5A
<b>Max. Current</b>	0.5A/point 3A/common
<b>Max Leakage Current</b>	0.01mA @ 40VDC
<b>Max Inrush Current</b>	3A/20ms 1A/100ms
<b>Minimum Load</b>	1mA
<b>Base Power Required</b>	9V (180mA max) 7.5mA/ON pt. 24V N/A
<b>OFF to ON Response</b>	0.1ms
<b>ON to OFF Response</b>	0.1ms
<b>Terminal Type</b>	Removable connector
<b>Status Indicators</b>	Logic side
<b>Weight</b>	7.1oz.(210g)
<b>Fuses</b>	2 (one 5A per common Non-replaceable)

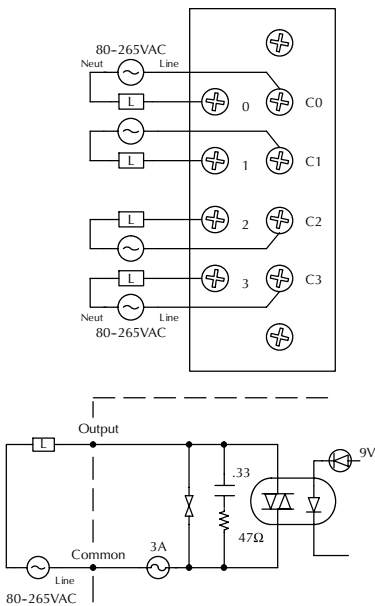
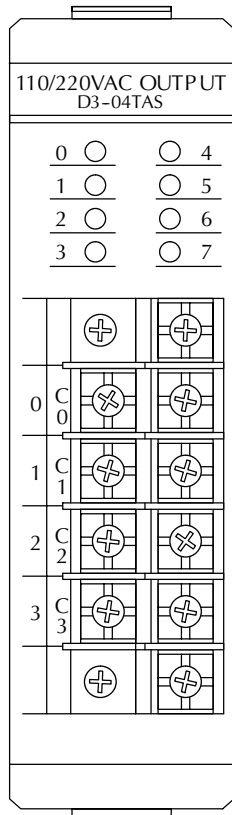
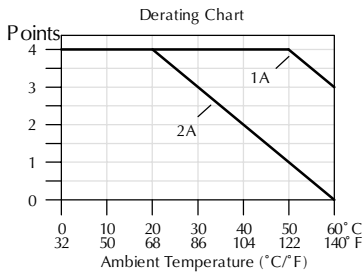
See the Connection Systems section in this desk reference for part numbers of ZIPLinks cables and terminal blocks compatible with this module.



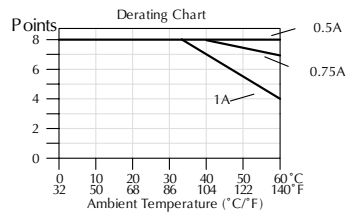
# AC OUTPUT MODULES

D3-04TAS AC Output <---->	
<b>Outputs per Module</b>	4
<b>Commons per Module</b>	4 (isolated)
<b>Operating Voltage</b>	80-265VAC
<b>Output Type</b>	Triac
<b>Peak Voltage</b>	265VAC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Drop</b>	1.5VAC @ 2A
<b>Max. Current</b>	2A/point 2A/common
<b>Max Leakage Current</b>	7mA @ 220VAC 3.5mA @ 110VAC
<b>Max Inrush Current</b>	20A for 16ms 10A for 100ms
<b>Minimum Load</b>	10mA
<b>Base Power Required</b>	9V 12mA max 24V N/A
<b>OFF to ON Response</b>	1ms max
<b>ON to OFF Response</b>	10ms max
<b>Terminal Type</b>	Non-removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	6.4oz. (180g)
<b>Fuses</b>	4 (1 per common) 3A Order D3-FUSE-2 (5 per pack) <---->

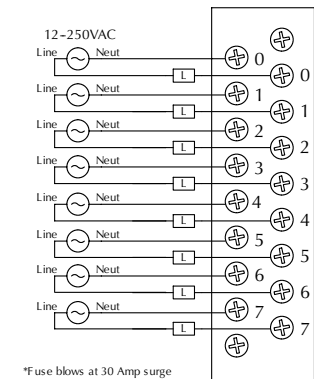
F3-08TAS AC Output <---->	
<b>Outputs per Module</b>	8 (500V point to point isolation)
<b>Commons per Module</b>	8 (isolated)
<b>Operating Voltage</b>	12-125VAC 125-250VAC <small>requires external fuses</small>
<b>Output Type</b>	SSR array (Triac)
<b>Peak Voltage</b>	400VAC
<b>AC Frequency</b>	47-440Hz
<b>ON Voltage Drop</b>	1VAC @ 1A
<b>Maximum Current</b>	1A/point
<b>Maximum Leakage Current</b>	10µ @ 240VAC
<b>Max Inrush Current*</b>	20A for 16ms 3A for 100ms
<b>Minimum Load</b>	0.5mA
<b>Base Power Required</b>	9V, 10mA/ON pt. (80mA max) 24V N/A
<b>OFF to ON Response</b>	8ms max
<b>ON to OFF Response</b>	8ms max
<b>Terminal Type</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	6.3oz. (179g)
<b>Fuses</b>	8 (1 per common) 5A, 125V fast blow Order D3-FUSE-4 (5 per pack) <---->



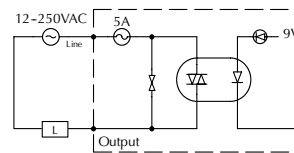
See the Connection Systems section in this desk reference for part numbers of ZIPLinks cables and terminal blocks compatible with this module.



Motor starters up to and including a NEMA size 3 can be used with this module.



\*Fuse blows at 30 Amp surge

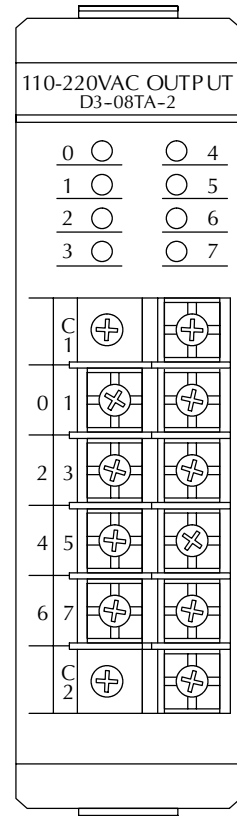
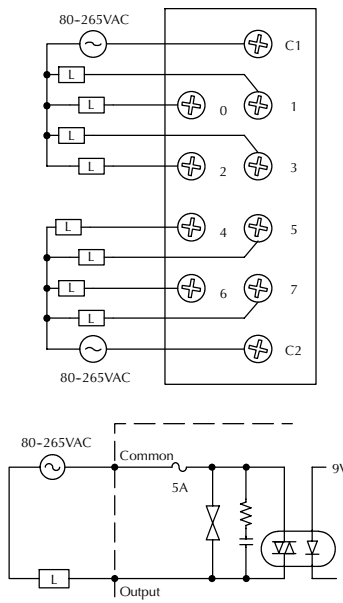
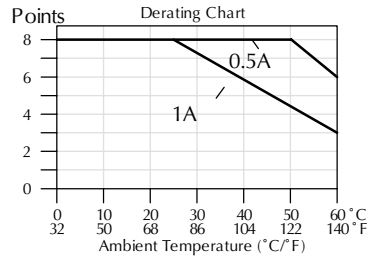
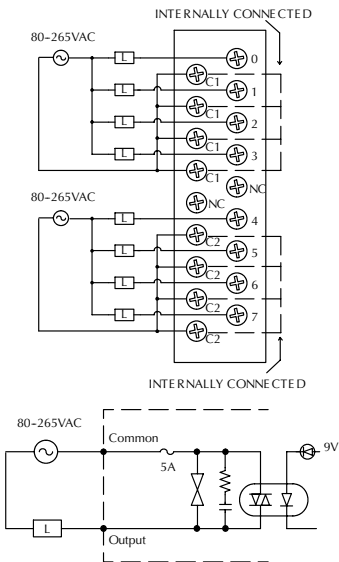
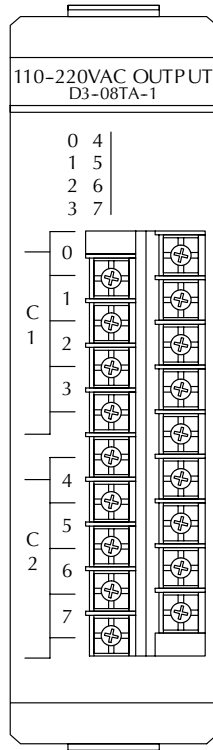
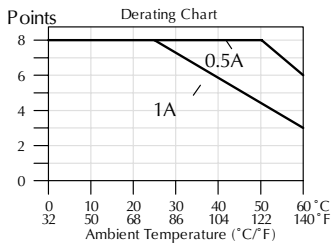


# AC OUTPUT MODULES

D3-08TA-1 AC Output <--->	
<b>Outputs per Module</b>	8
<b>Commons per Module</b>	2 (isolated)
<b>Operating Voltage</b>	80-265VAC
<b>Output Type</b>	Triac
<b>Peak Voltage</b>	265VAC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Drop</b>	1.5 VAC @ 1A
<b>Max. Current</b>	1A/point 3A/common
<b>Max Leakage Current</b>	1.2mA @ 220VAC 0.52mA @ 110VAC
<b>Max Inrush Current</b>	10A for 16ms 5A for 100ms
<b>Minimum Load</b>	25mA
<b>Base Power Required</b>	9V 20mA/ON pt. (160mA Max) 24V N/A
<b>OFF to ON Response</b>	1ms max
<b>ON to OFF Response</b>	8.33ms max
<b>Terminal Type</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	7.4oz. (210g)
<b>Fuses</b>	2 (one 5A per common) non-replaceable

D3-08TA-2 AC Output <--->	
<b>Outputs per Module</b>	8
<b>Commons per Module</b>	2 (isolated)
<b>Operating Voltage</b>	80-265VAC
<b>Output Type</b>	Triac
<b>Peak Voltage</b>	265VAC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Drop</b>	1.5 VAC @ 1A
<b>Max. Current</b>	1A/point 3A/common
<b>Max Leakage Current</b>	1.2mA @ 220VAC 0.52mA @ 110VAC
<b>Max Inrush Current</b>	10A for 16ms 5A for 100ms
<b>Minimum Load</b>	25mA
<b>Base Power Required</b>	9V 20mA/ON pt. (160mA Max) 24V N/A
<b>OFF to ON Response</b>	1ms max
<b>ON to OFF Response</b>	8.33ms max
<b>Terminal Type</b>	Non-removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	6.4oz. (180g)
<b>Fuses</b>	2 (one 5A per common) non-replaceable

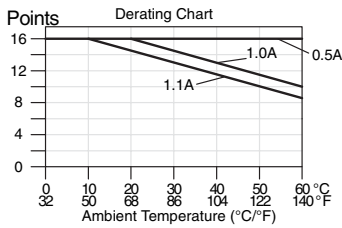
See the Connection Systems section in this desk reference for part numbers of ZIPLinks cables and terminal blocks compatible with this module.



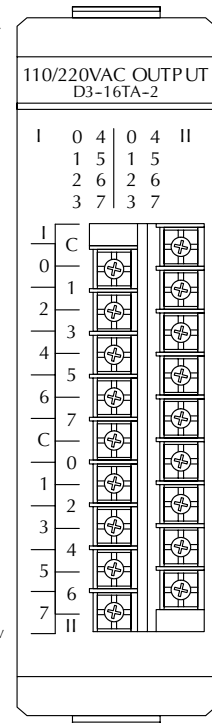
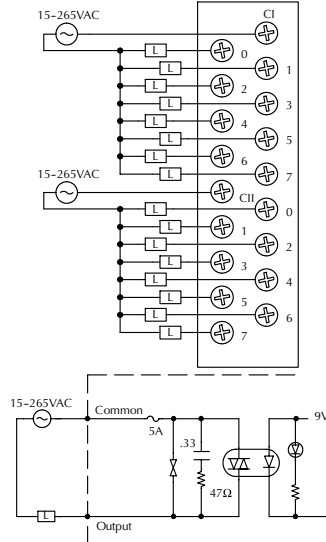
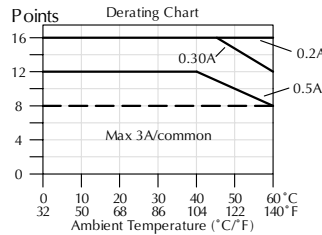
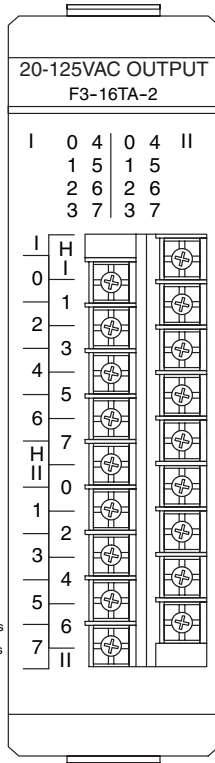
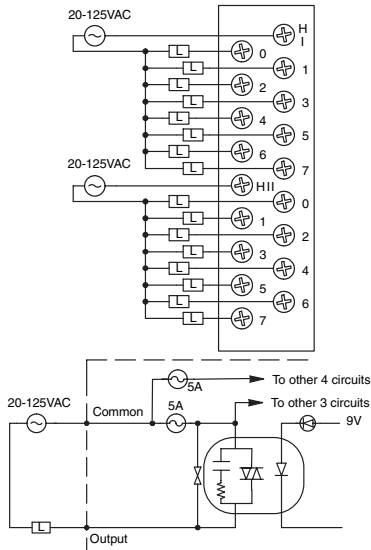
# AC OUTPUT MODULES

F3-16TA-2 AC Output <--->	
<b>Outputs per Module</b>	16
<b>Commons per Module</b>	2 (isolated)
<b>Operating Voltage</b>	20-125VAC
<b>Output Type</b>	SSR Array (TRIAC)
<b>Peak Voltage</b>	140VAC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Drop</b>	1.1VAC @ 1.1A
<b>Max. Current</b>	1.1A/point
<b>Max Leakage Current</b>	0.7mA @ 125VAC
<b>Max Inrush Current*</b>	15A for 20ms 8A for 100ms
<b>Minimum Load</b>	50mA
<b>Base Power Required</b>	9V 14mA / ON pt. 250mA Max. 24V N/A
<b>OFF to ON Response</b>	8ms max
<b>ON to OFF Response</b>	8ms max
<b>Terminal Type</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	7.7oz. (218g)
<b>Fuses (One spare fuse included)</b>	4 (One 5A 125V fast blow per each group of four outputs) Order D3-FUSE-4 (5 per pack) <--->

D3-16TA-2 AC Output <--->	
<b>Outputs per Module</b>	16
<b>Commons per Module</b>	2 (isolated)
<b>Operating Voltage</b>	15-265VAC
<b>Output Type</b>	Triac
<b>Peak Voltage</b>	265VAC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Drop</b>	1.5VAC @ 0.5A
<b>Max. Current</b>	0.5A/point 3A/common 6A/per module
<b>Max Leakage Current</b>	4 mA @ 265VAC
<b>Max Inrush Current</b>	10A for 10ms 5A for 100ms
<b>Minimum Load</b>	10 mA @ 15VAC
<b>Base Power Required*</b>	9V 25mA max/ON pt. 400mA max 24V N/A
<b>OFF to ON Response</b>	1ms max
<b>ON to OFF Response</b>	9ms max
<b>Terminal Type</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	7.2oz (210g)
<b>Fuses</b>	2 (One 5A per common) non-replaceable



\*Fuse blows at 20 Amp surge  
Motor starters up to and including a NEMA size 3 can be used with this module.





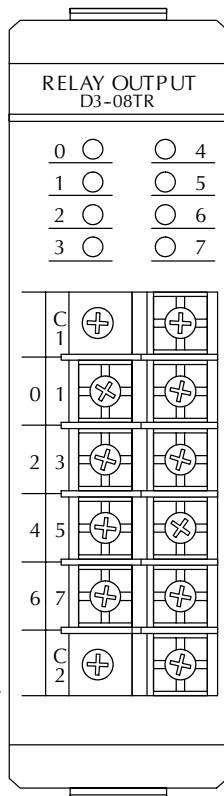
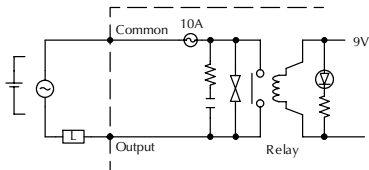
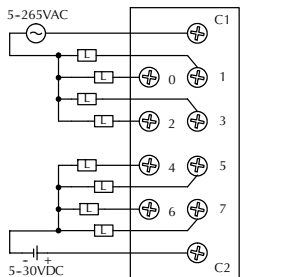
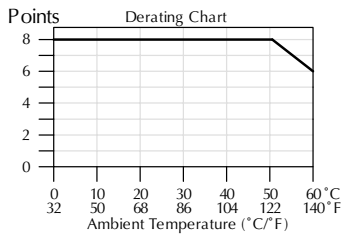
# RELAY OUTPUT MODULES

D3-08TR Relay Output <--->	
<b>Outputs per Module</b>	8
<b>Commons per Module</b>	2 (isolated)
<b>Operating Voltage</b>	5-265VAC 5-30VDC
<b>Output Type</b>	Form A (SPST)
<b>Peak Voltage</b>	265VAC/30VDC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Drop</b>	N/A
<b>Max. Current</b>	4A/point AC 5A/point DC 6A/common
<b>Max Leakage Current</b>	1mA @ 220VAC
<b>Max Inrush Current</b>	5A
<b>Minimum Load</b>	5mA @ 5V
<b>Base Power Required</b>	9V 45mA/ON pt. (360 mA max) 24V N/A
<b>OFF to ON Response</b>	5ms
<b>ON to OFF Response</b>	5 ms
<b>Terminal Type</b>	Non-removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	7oz. (200g)
<b>Fuses</b>	2 (one 10A per common) Order D3-FUSE-5 (5 per pack) <--->

D3-16TR Relay Output <--->	
<b>Outputs per Module</b>	16
<b>Commons per Module</b>	2 (isolated)
<b>Operating Voltage</b>	5-265VAC 5-30 VDC
<b>Output Type</b>	16 Form A (SPST)
<b>Peak Voltage</b>	265VAC/30VDC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Drop</b>	N/A
<b>Max. Current</b>	2A/point AC/DC (resistive) 8A/common AC/DC
<b>Max Leakage Current</b>	0.1mA @ 220VAC
<b>Max Inrush Current</b>	2A
<b>Minimum Load</b>	5mA @ 5V
<b>Base Power Required</b>	9V 30mA/ON pt. (480 mA max) 24V N/A
<b>OFF to ON Response</b>	12ms
<b>ON to OFF Response</b>	12ms
<b>Terminal Type</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	8.5oz. (248g)
<b>Fuses</b>	None

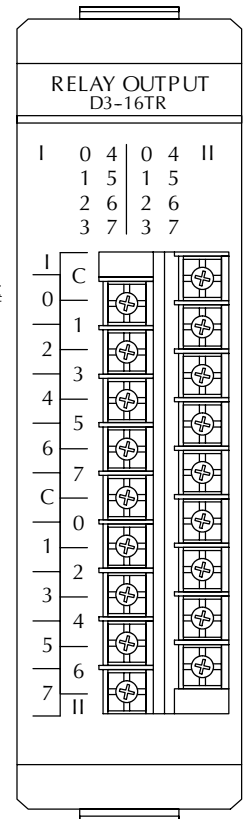
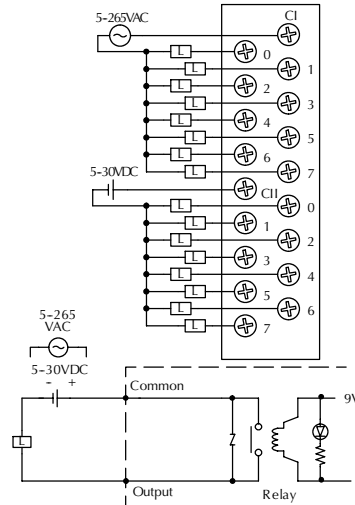
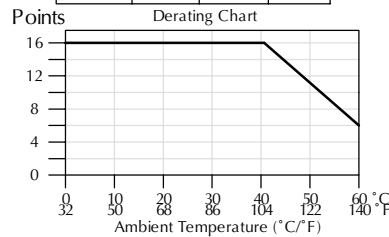
Typical Relay Life (Operations)

Voltage	Resistive	Solenoid	Closures
220VAC	4A	0.5A	100k
220VAC	0.05A	800k	
110VAC	4A	0.5A	100k
110VAC	0.1A	650k	
24VDC	5A	0.5A	100k



Typical Relay Life (Operations)

Voltage	Resistive	Solenoid	Closures
220VAC	2A	0.25A	100k
220VAC	0.03A	800k	
110VAC	2A	0.25A	100k
110VAC	0.05A	650k	
24VDC	2A	0.25A	100k



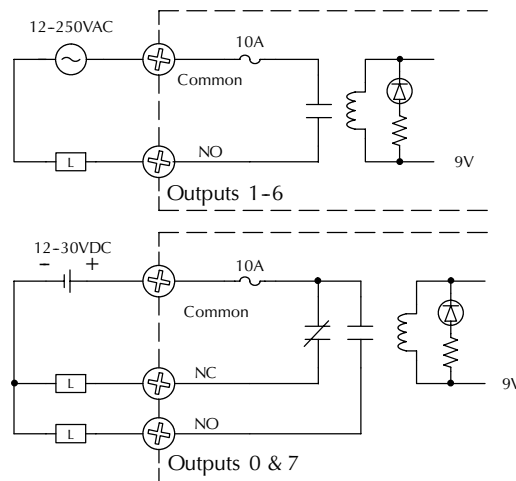
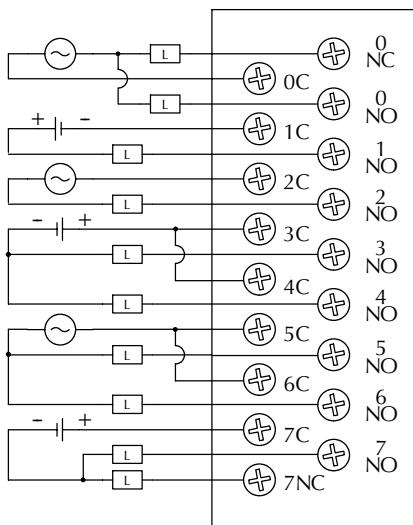
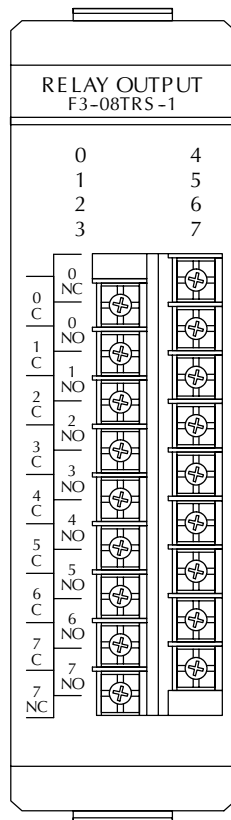
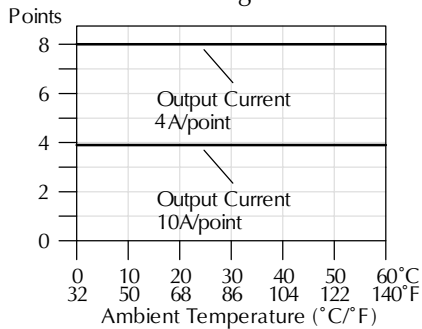
# RELAY OUTPUT MODULES

F3-08TRS-1 Relay Output <--->	
<b>Outputs per Module</b>	8
<b>Commons per Module</b>	8 (isolated)
<b>Operating Voltage*</b>	12-125VAC 125-250VAC (requires external fuses) 12-30VDC
<b>Output Type</b>	6 Form A (SPST), 2 Form C (SPDT)
<b>Max Current (resistive)</b>	10A/point AC/DC, 32A/module AC/DC
<b>Max leakage Current</b>	N/A
<b>Max Inrush Current</b>	10A inductive
<b>Minimum Load</b>	100mA @ 12VDC
<b>Base Power Required</b>	9V 37mA/ON pt., (296mA max), 24V N/A
<b>OFF to ON Response</b>	13ms max
<b>ON to OFF Response</b>	9ms max
<b>Terminal Type</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	8.9oz. (252 g)
<b>Fuses</b>	8 fuses (10A, 125V), Non-replaceable
<b>Peak Voltage</b>	265VAC/120VDC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Drop</b>	N/A

Max. Resistive or Inductive Inrush Load Current	Operating Voltage		
	28VDC	120VAC	240VAC
1/4HP		25K	
10.0A	50K	50K	
5.0A	200K	100K	
3.0A	325K	125K	50K
.05A	> 50M		

\*Maximum DC voltage rating is 120VDC at .5 Amp, 30,000 cycles typical. Motor starters up to and including NEMA size 4 can be used with this module.

Derating Chart



# RELAY OUTPUT MODULES

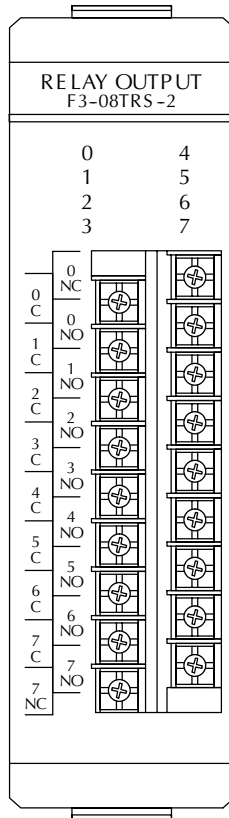
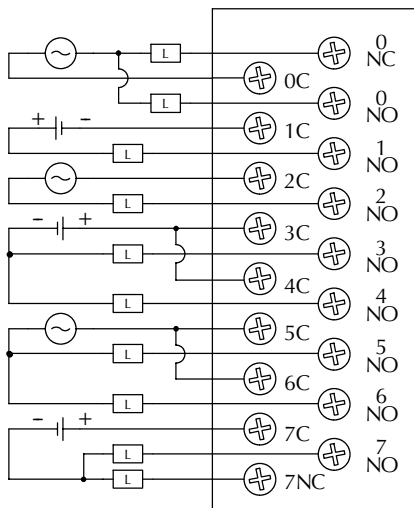
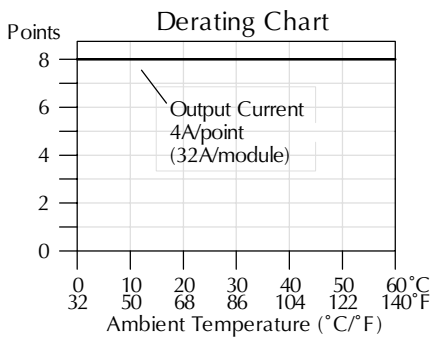
F3-08TRS-2 Relay Output <--->	
<b>Outputs per Module</b>	8
<b>Commons per Module</b>	8 (isolated)
<b>Operating Voltage*</b>	12-250VAC 12-30 VDC
<b>Output Type</b>	6 Form A (SPST), 2 Form C (SPDT)
<b>Peak Voltage</b>	265VAC/ 120VDC
<b>AC Frequency</b>	47-63Hz
<b>ON Voltage Drop</b>	N/A
<b>Max Current (Resistive)</b>	4A/point AC/DC, 32A/module AC/DC
<b>Max Leakage Current</b>	N/A

<b>Max Inrush Current</b>	10A inductive
<b>Minimum Load</b>	100mA @ 12VDC
<b>Base Power Required</b>	9V 37mA/ON pt. (296 mA Max), 24V N/A
<b>OFF to ON Response</b>	13ms max
<b>ON to OFF Response</b>	9ms max
<b>Terminal Type</b>	Removable
<b>Status Indicators</b>	Logic side
<b>Weight</b>	9oz. (255 g)
<b>Fuses</b>	8 fuses (10A, 125V), replaceable Order D3-FUSE-6 (5 per paze) <---> or use 19379-K-10A Wickman

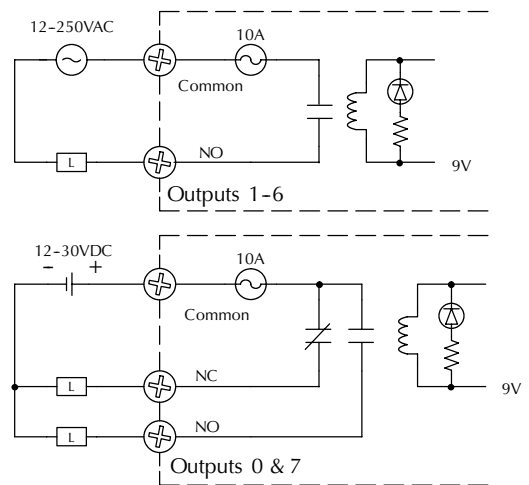
\*Maximum DC voltage rating is 120VDC at .5 Amp, 30,000 cycles typical.

Motor starters up to and including NEMA size 3 can be used with this module.

Typical Relay Life			
Max. Resistive or Inductive Inrush Load Current	Operating Voltage		
	28VDC	120VAC	240VAC
5.0A	200K	100K	
3.0A	325K	125K	50K
0.5A	>50M		



Expected mechanical relay life is 100 million operations.



# ANALOG INPUT MODULES

F3-04ADS 4-Channel Isolated Analog Input <--->	
<b>Number of Channels</b>	4, (isolated)
<b>Input Ranges</b>	0-5V, 0-10V, 1-5V, $\pm 5V$ , $\pm 10V$ , 0-20mA, 4-20mA
<b>Channels Individually Configured</b>	Yes
<b>Resolution</b>	12 bit (1 in 4096)
<b>Input Type</b>	Differential
<b>Max. Common Mode Voltage</b>	$\pm 750V$ peak continuous transformer isolation
<b>Noise Rejection Ratio</b>	Common mode: -100dB at 60Hz
<b>Active Low-pass Filtering</b>	-3dB at 10Hz, -12dB per octave
<b>Input Impedance</b>	250ohm $\pm 0.1\%$ , 1/2W current input, 200Kohm voltage input
<b>Absolute Maximum Ratings</b>	$\pm 40$ mA, current input $\pm 100V$ , voltage input
<b>Conversion Time</b>	1 channel per scan, successive approximation, AD574
<b>Linearity Error</b>	$\pm 1$ counts max. (0.03% of full scale) unipolar $\pm 2$ counts max. (0.05% of full scale) bipolar
<b>Full Scale Calibration Error</b>	$\pm 8$ counts maximum

<b>Offset Calibration Error</b>	$\pm 8$ counts maximum
<b>Accuracy vs. Temperature</b>	57ppm/ °C maximum full scale
<b>Recommended Fuse</b>	0.032A, Series 217 fast-acting, current inputs
<b>Power Budget Requirement</b>	183mA @ 9VDC, 50mA @ 24VDC
<b>External Power Supply</b>	None required
<b>Operating Temperature</b>	32° to 140°F (-0° to 60°C)
<b>Storage Temperature</b>	-4° to 158° F (-20° to 70°C)
<b>Relative Humidity</b>	5 to 95% (non-condensing)
<b>Environmental Air</b>	No corrosive gases permitted
<b>Vibration</b>	MIL STD 810C 514.2
<b>Shock</b>	MIL STD 810C 516.2
<b>Noise Immunity</b>	NEMA ICS3-304

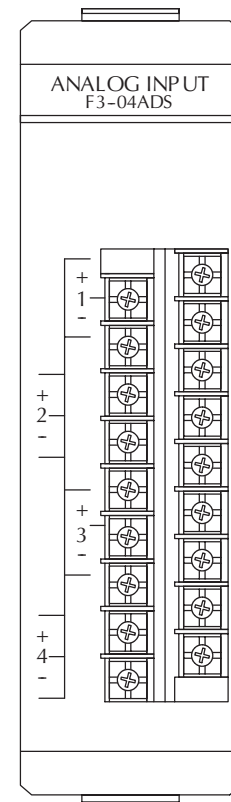
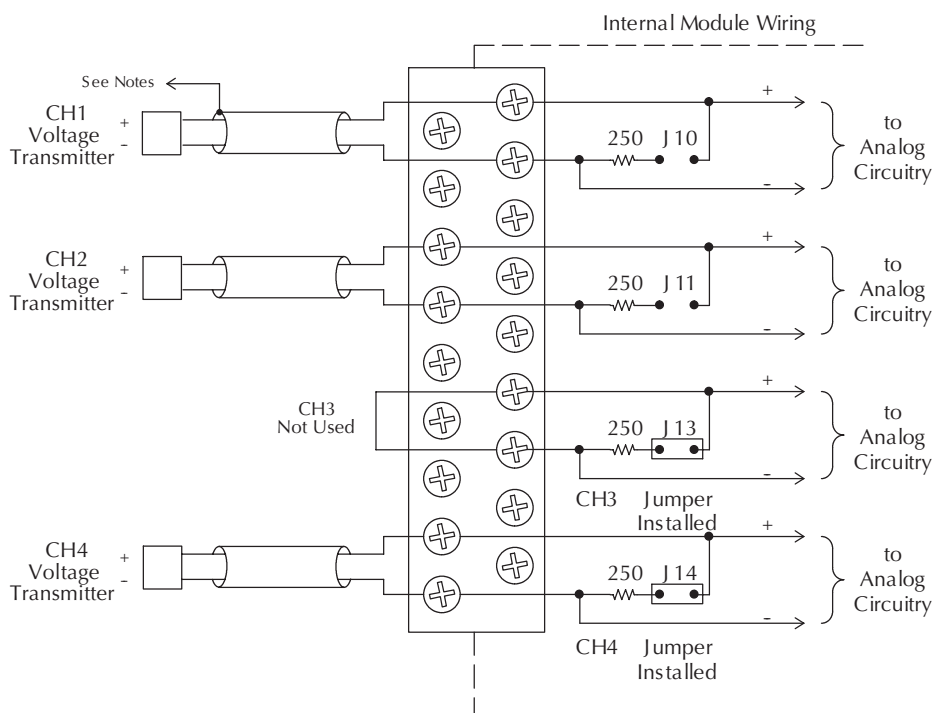
**Note 1:** Connect unused voltage or current inputs to 0VDC at terminal block or leave current jumper installed (see Channel 3).

**Note 2:** A Series 217, 0.032A, fast-acting fuse is recommended for 4-20mA current loops.

**Note 3:** Transmitters may be 2, 3, or 4 wire type.

**Note 4:** Transmitters may be powered from separate power sources.

**Note 5:** Terminate all shields of the cable at their respective signal source.



# ANALOG INPUT MODULES

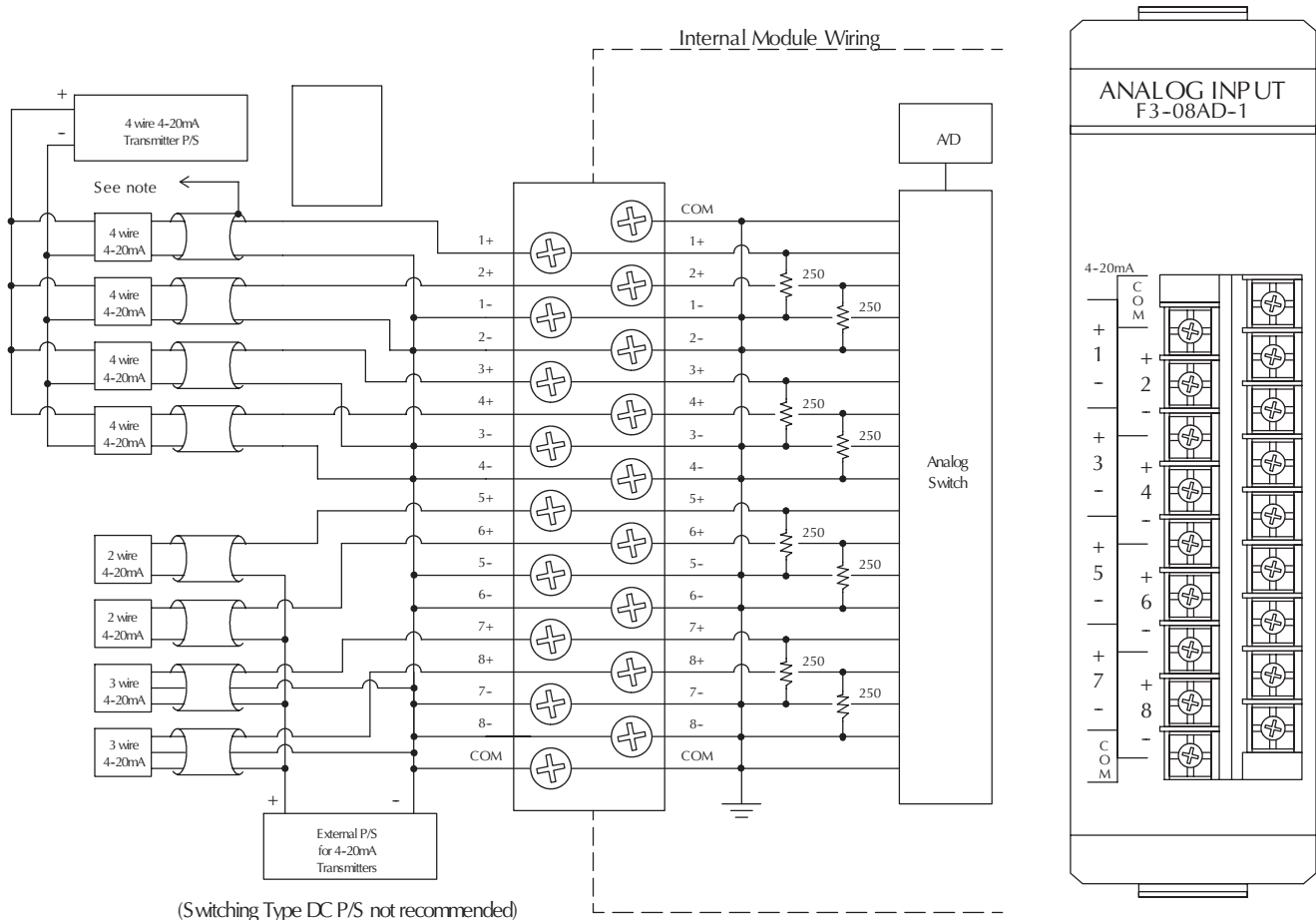
F3-08AD-1 8-Channel Analog Input <---> (Replaces F3-08AD)	
<b>Number of Channels</b>	8, single ended (one common)
<b>Input Ranges</b>	4-20mA
<b>Resolution</b>	12 bit (1 in 4096)
<b>Low Pass Filter</b>	-3db @ 200Hz (-6db/octave)
<b>Input Impedance</b>	250ohm ± 0.1%, 1/2W current input
<b>Absolute Maximum Ratings</b>	±40mA
<b>Conversion Time</b>	1 channel per CPU scan
<b>Converter Type</b>	Successive approximation, MAX170
<b>Linearity Error</b>	± 1 count (0.03% of full scale) maximum
<b>Input Stability</b>	±0.05 count
<b>Maximum Inaccuracy</b>	0.1% of full scale at 77°F (25°C)
<b>Accuracy vs. Temperature</b>	57ppm/°C maximum full scale (including maximum offset change of 2 counts)

<b>Recommended Fuse</b>	0.032A, Series 217 fast-acting
<b>Power Budget Requirement</b>	45mA @9 VDC, 55mA @ 24VDC
<b>External Power Supply</b>	None required
<b>Operating Temperature</b>	32° to 140°F (0° to 60°C)
<b>Storage Temperature</b>	-4° to 158°F (-20° to 70°C)
<b>Relative Humidity</b>	5 to 95% (non-condensing)
<b>Environmental air</b>	No corrosive gases permitted
<b>Vibration</b>	MIL STD 810C 514.2
<b>Shock</b>	MIL STD 810C 516.2
<b>Noise Immunity</b>	NEMA ICS3-304

Note 1: Terminate all shields at their respective signal source.

Note 2: To avoid "ground loop" errors, the following transmitter types are recommended:  
 2 and 3 wire: Isolation between input signal and P/S  
 4 wire: Full isolation between input signal, P/S and output signal.

Note 3: A Series 217 0.032A fast-acting fuse is recommended for 4-20mA applications.

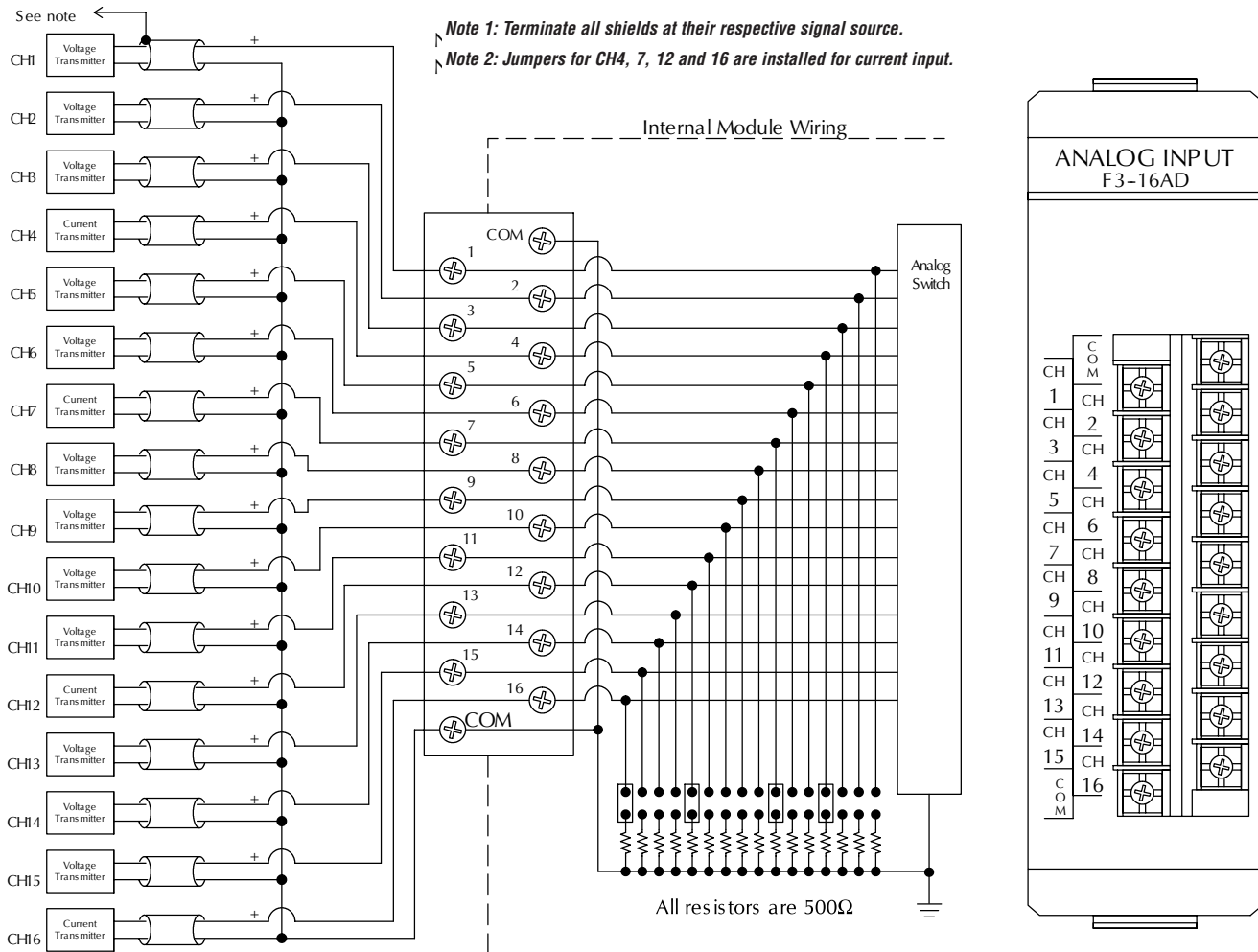


# ANALOG INPUT MODULES

F3-16AD 16-Channel Analog Input <---->	
<b>Number of Channels</b>	16, single ended (one common)
<b>Input Ranges</b>	±5V, ±10V, 0-5V <sup>1</sup> , 0-10V, 0-20mA, 4-20mA <sup>2</sup>
<b>Channels Individually Configured</b>	Range is selected for all channels. Each channel can be wired for voltage or current.
<b>Resolution</b>	12 bit (1 in 4096)
<b>Input Impedance</b>	2Mohm, voltage input, 500ohm ±1% current input
<b>Absolute Maximum Ratings</b>	±25mA, voltage input ±30mA, current input
<b>Conversion Time</b>	35µs per channel, 1 channel per CPU scan
<b>Converter Type</b>	Successive Approximation, AD574
<b>Linearity Error</b>	±1 count maximum
<b>Maximum Inaccuracy at 77°F (25°C)</b>	0.25% of full scale, voltage input 1.25% of full scale, current input
<b>Accuracy vs. Temperature</b>	57ppm/°C maximum full scale

<b>Recommended Fuse</b>	0.032 A, Series 217 fast-acting, current inputs
<b>Power Budget Requirement</b>	55mA @ 9VDC, 65mA @ 24VDC
<b>External Power Supply</b>	None required
<b>Operating Temperature</b>	32° to 140°F (0° to 60 C)
<b>Storage Temperature</b>	-4° to 158°F (-20° to 70°C)
<b>Relative Humidity</b>	5 to 95% (non-condensing)
<b>Environmental Air</b>	No corrosive gases permitted
<b>Vibration</b>	MIL STD 810C 514.2
<b>Shock</b>	MIL STD 810C 516.2
<b>Noise Immunity</b>	NEMA ICS3-304

• requires gain adjustment with potentiometer.  
 • resolution is 3275 counts (instead of 4096). Allows easier broken transmitter detection.

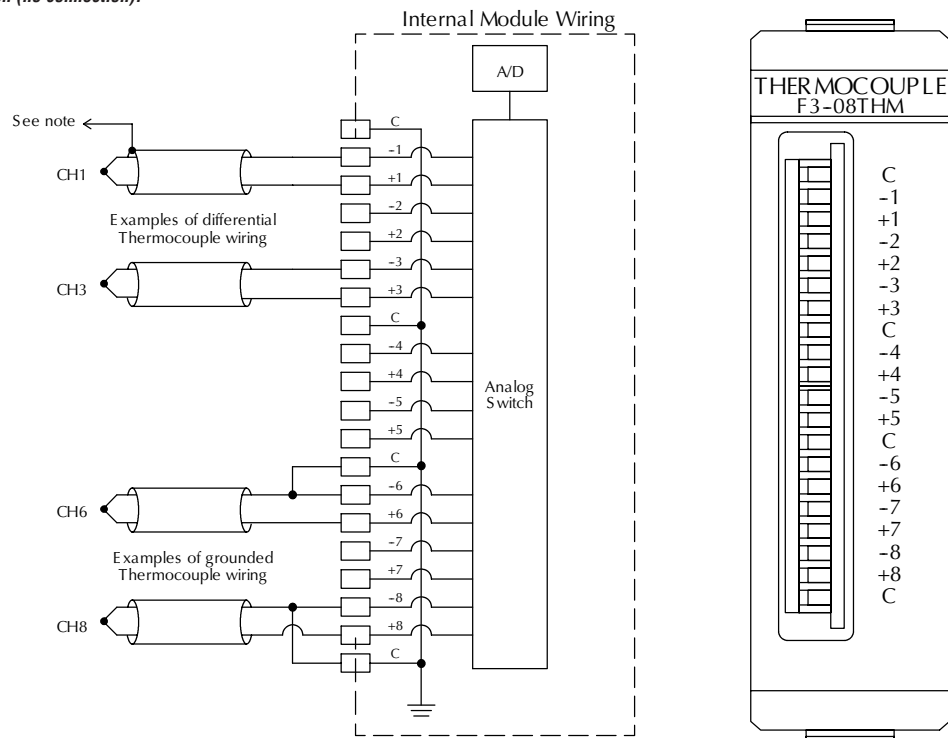


# TEMPERATURE INPUT MODULES

F3-08THM-n 8-Channel Thermocouple Input <--->	
<i>Note: When you order the module, replace the "n" with the type of thermocouple needed. For example, to order a Type J thermocouple module, order part number F3-08THM-J or F3-08THM-K for type K. Types J and K are stock. All other are special order.</i>	
<b>Input Ranges</b>	Type E: -270/1000°C, -450/1832°F (obsolete) Type J: -210/760°C, -350/1390°F Type K: -270/1370°C, -450/2500°F Type R: 0/1768°C, 32/3214°F (obsolete) Type S: 0/1768°C, 32/3214°F (obsolete) Type T: -270/400°C, -450/752°F (obsolete) -1: 0-50mV -2: 0-100mV
<b>Resolution</b>	12 bit (1 in 4096)
<b>Input Impedance</b>	27Kohm DC
<b>Absolute Maximum Ratings</b>	Fault protected input, 130 Vrms or 100 VDC
<b>Cold Junction Compensation</b>	Automatic
<b>Conversion Time</b>	15ms per channel, minimum 1 channel per CPU scan

<b>Converter Type</b>	Successive approximation, AD574
<b>Linearity Error</b>	±1 count (0.03% of full scale) maximum
<b>Maximum Inaccuracy at 77°F (25°C)</b>	0.35% of full scale
<b>Accuracy vs. Temperature</b>	57ppm/°C maximum full scale
<b>Power Budget Requirement</b>	50mA @ 9VDC, 34mA @ 24VDC
<b>External Power Supply</b>	None required
<b>Operating Temperature</b>	32° to 140°F (0° to 60°C)
<b>Storage Temperature</b>	-4° to 158°F (-20° to 70°C)
<b>Relative Humidity</b>	5 to 95% (non-condensing)
<b>Environmental Air</b>	No corrosive gases permitted
<b>Vibration</b>	MIL STD 810C 514.2
<b>Shock</b>	MIL STD 810C 516.2
<b>Noise Immunity</b>	NEMA ICS3-304

**Note 1:** Terminate shields at the respective signal source.  
**Note 2:** Leave unused channel open (no connection).



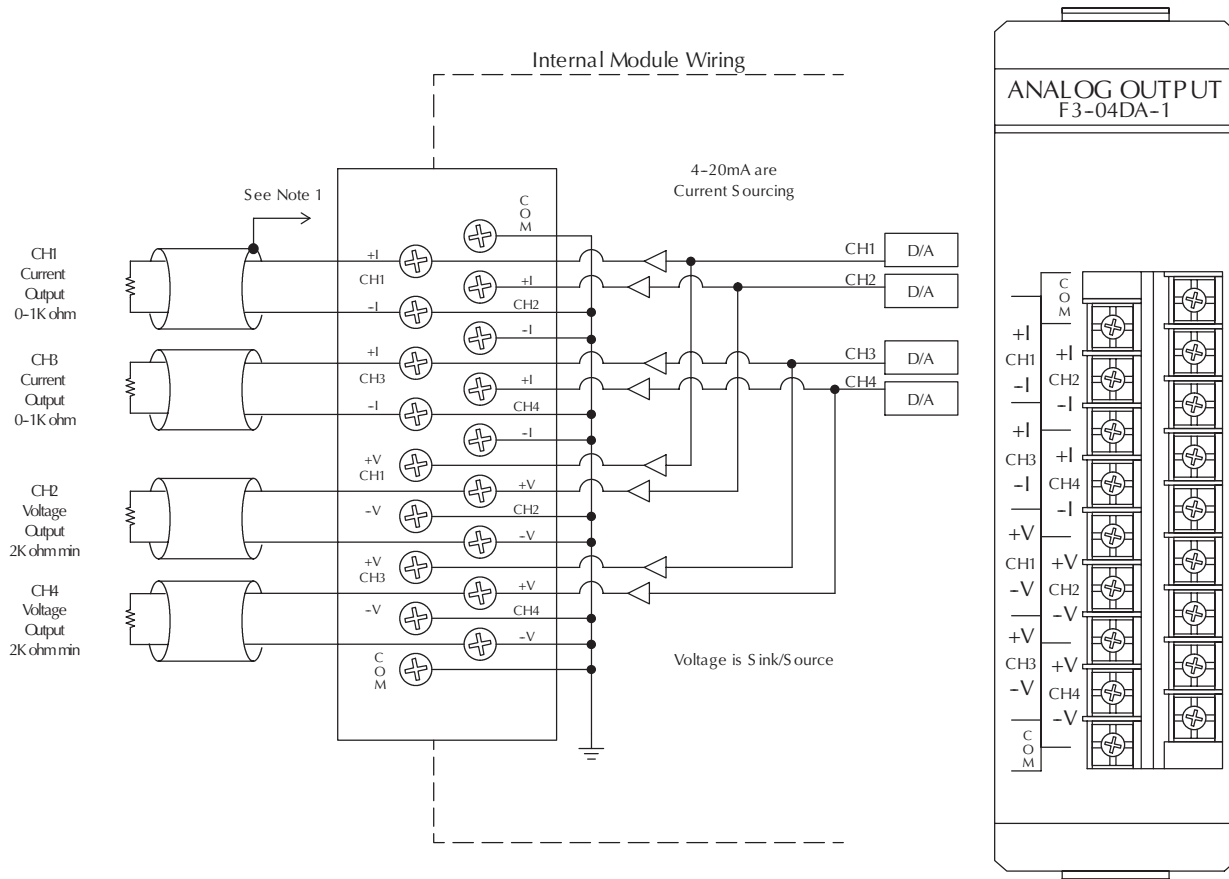
# ANALOG OUTPUT MODULES

F3-04DA-1 4-Channel Analog Output <---->	
<b>Number of Channels</b>	4
<b>Output Range</b>	0 - 5V, 0-10V 4-12 mA, 4-20 mA (source)
<b>Channels Individually Configured</b>	Yes
<b>Resolution</b>	12-bit (1 in 4096)
<b>Output Type</b>	Single ended (one common)
<b>Output Impedance</b>	0.5ohm typical, voltage output
<b>Output Current</b>	5mA source, 2.5mA sink (voltage)
<b>Short-circuit Current</b>	40mA typical, voltage output
<b>Load Impedance</b>	1Kohm maximum, current output 2Kohm minimum, voltage output
<b>Linearity Error</b>	±1 count (±0.03% maximum)
<b>Maximum Inaccuracy at 77° F (25° C)</b>	± 0.6% of span, current output ± 0.2% of span, voltage output

<b>Accuracy vs. Temperature</b>	±50ppm/°C maximum
<b>Conversion Time</b>	30µS maximum
<b>Power Budget Requirement</b>	144mA @ 9V, 108mA @24V
<b>External Power Supply</b>	None required
<b>Operating Temperature</b>	32° to 140°F (0° to 60°C)
<b>Storage Temperature</b>	-4° to 158°F (-20° to 70°C)
<b>Relative Humidity</b>	5 to 95% (non-condensing)
<b>Environmental Air</b>	No corrosive gases permitted
<b>Vibration</b>	MIL STD 810C 514.2
<b>Shock</b>	MIL STD 810C 516.2
<b>Noise Immunity</b>	NEMA ICS3-304

**Note 1:** Shields should be connected to the 0V (COM) of the module.

**Note 2:** Unused voltage and current outputs should remain open (no connections).





# ANALOG OUTPUT MODULES

F3-04DAS 4-Channel Isolated Analog Output <--->	
<b>Number of Channels</b>	4
<b>Output Ranges</b>	±5V, ±10V, 0 - 5V, 0 - 10V, 1 - 5V 0-20mA, 4-20mA
<b>Channels Individually Configurable</b>	Yes
<b>Resolution</b>	12-bit (1 in 4096)
<b>Output Type</b>	Isolated, 750 VDC channel-to-channel 750 VDC channel-to-logic
<b>Output Current</b>	±5mA voltage output
<b>Short-circuit Current</b>	±20mA typical, voltage output
<b>Capacitive Load Drive</b>	0.1µF typical, voltage output
<b>Load Impedance</b>	470ohm maximum, current output 2Kohm minimum, voltage output
<b>Isolation Mode Rejection</b>	140dB at 60Hz
<b>Linearity Error</b>	±1 count (±0.03% maximum)
<b>Calibration Error</b>	±0.15% typical, ±0.75% max. of span ±10 ppm/°C maximum of full scale

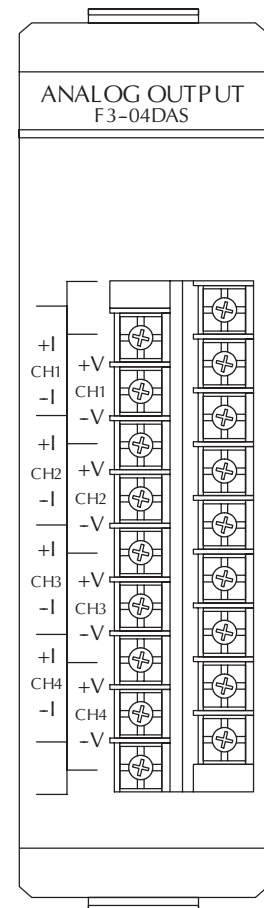
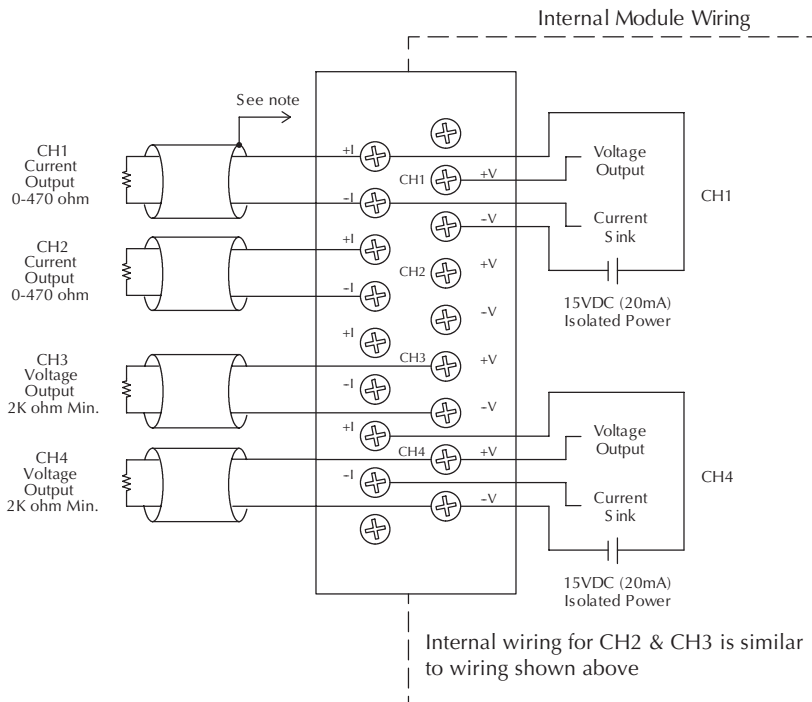
<b>Calibrated Offset Error</b>	±1 count maximum, current output ±5 mV typical, ±50mV max., voltage out ±0.2mV typical /°C
<b>Conversion Time</b>	30µS maximum, 1 channel/scan
<b>Power Budget Requirement</b>	154mA @ 9V, 145mA @ 24V
<b>External Power Supply</b>	None required
<b>Operating Temperature</b>	32° to 140°F (0° to 60°C)
<b>Storage Temperature</b>	-4° to 158°F (-20° to 70°C)
<b>Relative Humidity</b>	5 to 95% (non-condensing)
<b>Environmental Air</b>	No corrosive gases permitted
<b>Vibration</b>	MIL STD 810C 514.2
<b>Shock</b>	MIL STD 810C 516.2
<b>Noise Immunity</b>	NEMA ICS3-304

**Note 1:** Shields should be connected to the respective channel's -V terminal of the module.

**Note 2:** Each isolated output channel may have either a voltage or current load, but not both.

**Note 3:** An external 0.31 Amp fast-acting fuse in series with the isolated +I terminal (+15VDC) is recommended to protect against accidental shorts to the -V terminal (15VDC common).

**Note 4:** Do not attempt to source more than 20mA from any one of the four isolated 15VDC power supplies.



# DL305 INSTRUCTION SET

## Boolean Instructions

### Store (STR)

Begins a new rung or an additional branch in a rung with a normally open contact.

### Store Not (STRN)

Begins a new rung or an additional branch in a rung with a normally closed contact.

### Store timer (STR TMR) D3-330/340 only

Begins a new rung or additional branch in a rung with a normally open timer contact.

### Store not timer (STR NOT TMR) D3-330/340 only

Begins a new rung or additional branch in a rung with a normally closed timer contact.

### Store counter (STR CNT) D3-330/340 only

Begins a new rung or additional branch in a rung with a normally open counter contact.

### Store not counter (STR NOT CNT) DL330/DL340 only

Begins a new rung or additional branch in a rung with a normally closed counter contact.

### Or (OR)

Logically Ors a normally open contact in parallel with another contact in a rung.

### Or not (OR NOT)

Logically Ors a normally closed contact in parallel with another contact in a rung.

### Or timer (OR TMR) D3-330/340 only

Logically Ors a normally open timer contact in parallel with another contact in a rung.

### Or not timer (OR NOT TMR) D3-330/340 only

Logically Ors a normally closed timer contact in parallel with another contact in a rung.

### Or counter (OR CNT) D3-330/340 only

Logically Ors a normally open counter contact in parallel with another contact in a rung.

### Or not counter (OR NOT CNT) D3-330/340 only

Logically Ors a normally closed counter contact in parallel with another contact in a rung.

### And (AND)

Logically ANDs a normally open contact in series with another contact in a rung.

### And not (ANDN)

Logically ANDs a normally closed contact in series with another contact in a rung.

### And timer (AND TMR) D3-330/340 only

Logically ANDs a normally open timer contact in series with another contact in a rung.

### And not timer (AND NOT TMR) D3-330/340 only

Logically ANDs a normally closed timer in series with another contact in a rung.

### And counter (AND CNT) D3-330/340 only

Logically ANDs a normally open counter contact in series with another contact in a rung.

### And not counter (AND NOT CNT) D3-330/340 only

Logically ANDs a normally closed counter contact in series with another contact in a rung.

### And store (AND STR)

Logically ANDs two branches in a rung in series.

### Or store (OR STR)

Logically Ors two branches of a rung in parallel.

### Out (OUT)

Reflects the status of the rung (ON/OFF) and outputs the discrete (ON/OFF) state to the specified image register.

### Set (SET) D3-330/340 only

Sets or turns on an output. Once the output is set it will remain on until it is reset using the RST instruction or by a result of the ladder logic execution.

### Reset (RST)

Resets or turns OFF an output or resets a counter.

### Set out (SET OUT)

Reflects the status of the rung (ON/OFF) and outputs the discrete (ON/OFF) state to the specified image register.

### Set out reset (SET OUT RST)

Typically known as a one shot, when the input logic produces an OFF to ON transition the output will turn ON for one CPU scan.

### Master control set (MCS)/Master control reset (MCR)

The Master control set and Master Control Reset instructions are used to provide an additional left power rail which is controllable by an input contact. This is sometimes known as a sub power rail. Any number of rungs of ladder logic can be disabled using these instructions.

## Comparative Boolean Instructions

### Store, if equal (STRE) D3-330/340 only

Begins a new rung or additional branch in a rung with a normally open comparative counter contact. The contact will be ON if C aaa = B bbbb.

### Store not, if equal (STR N) D3-330/340 only

Begins a new rung or additional branch in a rung with a normally closed comparative counter contact. The contact will be ON if C aaa ≠ B bbbb.

### Or, if equal (ORE) D3-330/340 only

Connects a normally open comparative counter contact in parallel with another contact. The contact will be ON if C aaa = B bbbb.

### Or not, if equal (OR N) D3-330/340 only

Connects a normally closed comparative counter contact in parallel with another contact. The contact will be ON if C aaa ≠ B bbbb.

### And, if equal (ANDE) D3-330/340 only

Connects a normally open comparative counter contact in series with another contact. The contact will be ON if C aaa = B bbbb.

### And not, if equal (ANDNE) D3-330/340 only

Connects a normally closed comparative counter contact in series with another contact. The contact will be ON if C aaa ≠ B bbbb.

## Accumulator Load and Output Instructions

### Data store (F50)

Loads the value of a 16-bit register, two consecutive 8-bit registers, or a 4-digit BCD value into the accumulator.

### Data store 1 (F51)

Loads the value from a specified 8-bit register into the lower 8 bits of the accumulator.

### Data store 2 (F52)

Loads the value of the most significant 4 bits of a specified 8 bit register into the least significant 4 bits of the accumulator.

### Data store 3 (F53)

Loads the value of the least significant 4 bits of a specified 8 bit register into the least significant 4 bits of the accumulator.

### Data store 5 (F55)

Loads the value of 16-image register locations for a specified 16-point input module into the accumulator.

### Data out (F60)

Copies the 16-bit value in the accumulator to a 16-bit reference or two consecutive 8-bit registers.

### Data out 1 (F61)

Copies the value in the lower 8 bits of the accumulator to a specified 8-bit register.

### Data out 2 (F62)

Copies the value in the least significant 4 bits of the accumulator into the most significant 4 bits of a specified 8-bit register.

### Data out 3 (F63)

Copies the value in the least significant 4 bits of the accumulator to the least significant 4 bits of a specified 8-bit register.

### Data out 5 (F65)

Copies the 16-bit value in the accumulator to the image register of a specified 16 point output module.

## Bit Operation Instructions

### Shift left (F80)

Shifts the value in the accumulator a specified number of bits (15 maximum) to the left.

### Shift right (F81)

Shifts the value in the accumulator a specified number of bits (15 maximum) to the right.

### Decode (F82)

Decodes a 4-bit binary number in the accumulator by setting the appropriate bit position to a one.

### Encode (F83)

Encodes the accumulator bit position that contains a 1 by returning the appropriate 4-bit binary representation.

### Binary (F85)

Converts a BCD value in the accumulator to the binary/HEX equivalent value.

### Binary coded decimal (F86)

Converts a binary/HEX equivalent value in the accumulator to the BCD equivalent.

### Inverse (F84)

Generates the one's complement of the number in the accumulator.

## Accumulator Logic Instructions

### Data and (F75)

Logically ANDs the value in a 16-bit reference, two consecutive 8-bit registers, or a 4-digit BCD constant with the value in the accumulator.

### Data or (F76)

Logically ORs the value in a 16-bit reference, two consecutive 8-bit registers, or 4-digit BCD constant with the value in the accumulator.

### Compare (F70)

Compares the value in a 16-bit reference, two consecutive 8-bit registers, or 4-digit BCD constant with the value in the accumulator.

## Timer, Counter and Shift Register Instructions

### Timer (TMR) D3-330/340 only

Provides a single input timer with a 0.1 second increment (0-999.9 seconds) in the normal operating mode, or a 0.01 second increment (0-99.99 seconds) in the fast timer mode.

### Counter (CNT) D3-330/340 only

Provides a counter with a count and reset input and a range of 0-9999.

### Shift register (SR) D3-330/340 only

Shifts data through a predefined number of shift register bits (up to 128 bits).

## Math Instructions

### Add (F71)

Adds the value of a 16-bit reference, two consecutive 8 bit registers, or a 4-digit BCD constant with the value in the accumulator.

### Subtract (F72)

Subtracts the value in a 16-bit register, two consecutive 8-bit registers, or a 4-digit BCD constant from the value in the accumulator.

### Multiply (F73)

Multiplies the value in a 16-bit register, two consecutive 8-bit registers, or a 4-digit BCD constant by the value in the accumulator.

### Divide (F74)

Divides the value in the accumulator by the value in a 16-bit register, two consecutive 8-bit registers, or a 4-digit BCD constant.

## Message Instructions

### Fault (F20)

Used to display a 4-digit BCD constant, 16-bit register, or two consecutive 8-bit data registers on the handheld programmer or DirectSoft32.

# D3-350 INSTRUCTION SET

## Boolean Instructions

- Store (STR)**  
Begins a new rung or an additional branch in a rung with a normally open contact.
- Store not (STR NOT)**  
Begins a new rung or an additional branch in a rung with a normally closed contact.
- Or (OR)**  
Logically ORS a normally open contact in parallel with another contact in a rung.
- Or Not (OR NOT)**  
Logically ORS a normally closed contact in parallel with another contact in a rung.
- And (AND)**  
Logically ANDS a normally open contact in parallel with another contact in a rung.
- And Not (AND NOT)**  
Logically ANDS a normally closed contact in parallel with another contact in a rung.
- And Store (AND STR)**  
Logically ANDS two branches of a rung in series.
- Or Store (OR STR)**  
Logically ORS two branches of a rung in parallel.
- Out (OUT)**  
Reflects the status of the rung (on/off) and outputs the discrete (on/off) state to the specified image register point or memory location.
- Or Out (OR OUT)**  
Reflects the status of the rung and outputs the discrete (ON/OFF) state to the image register. Multiple OR OUT instructions referencing the same discrete point can be used in the program.
- Not (NOT)**  
Inverts the status of the rung at the point of the instruction.
- Positive Differential (PD)**  
Is typically known as a one shot. When the input logic produces an off to on transition, the output will energize for one CPU scan.
- Set (SET)**  
An output that turns on a point or a range of points. The reset instruction is used to turn the point(s) OFF that were set ON with the set instruction.
- Reset (RST)**  
An output that resets a point or a range of points.
- Pause outputs (PAUSE)**  
Disables the update for a range of specified output points.

## Comparative Boolean Instructions

- Store if Equal (STR E)**  
Begins a new rung or additional branch in a rung with a normally open comparative contact. The contact will be on when  $A = B$ .
- Store if Not Equal (STR NOT E)**  
Begins a new rung or additional branch in a rung with a normally closed comparative contact. The contact will be on when  $A \neq B$ .
- Or if Equal (OR E)**  
Connects a normally open comparative contact in parallel with another contact. The contact will be on when  $A = B$ .
- Or if Not Equal (OR NOT E)**  
Connects a normally closed comparative contact in parallel with another contact. The contact will be on when  $A \neq B$ .
- And if Equal (AND E)**  
Connects a normally open comparative contact in series with another contact. The contact will be on when  $A = B$ .
- And if Not Equal (AND NOT E)**  
Connects a normally closed comparative contact in series with another contact. The contact will be on when  $A \neq B$ .
- Store (STR)**  
Begins a new rung or additional branch in a rung with a normally open comparative contact. The contact will be on when  $A \geq B$ .
- Store not (STR NOT)**  
Begins a new rung or additional branch in a rung with a normally closed comparative contact. The contact will be on when  $A > B$ .
- Or (OR)**  
Connects a normally closed comparative contact in parallel with another contact. The contact will be on when  $A \geq B$ .
- Or Not (OR NOT)**  
Connects a normally closed comparative contact in parallel with another contact. The contact will be on when  $A < B$ .
- And (AND)**  
Connects a normally open comparative contact in series with another contact. The contact will be on when  $A \geq B$ .
- And Not (AND NOT)**  
Connects a normally closed comparative contact in series with another contact. The contact will be on when  $A < B$ .

## Bit of Word Boolean Instructions

- Store Bit of Word (STRB)**  
Begins a new rung or an additional branch in a rung with a normally open contact that examines single bit of a V-memory location.
- Store Not Bit of Word (STRNB)**  
Begins a new rung or an additional branch in a rung with a normally closed contact that examines single bit of a V-memory location.
- Or Bit of Word (ORB)**  
Logically ORS a normally open bit of word contact in parallel with another contact in a rung.
- Or Not Bit of Word (ORNB)**  
Logically ORS a normally closed bit of word contact in parallel with another contact in a rung.
- And Bit of Word (ANDB)**  
Logically ANDS a normally open bit of word contact in series with another contact in a rung.
- And Not Bit of Word (ANDNB)**  
Logically ANDS a normally closed bit of word contact in series with another contact in a rung.
- Out Bit of Word (OUTB)**  
Reflects the status of the rung (on/off) and outputs the discrete (on/off) state to the specified bit of a V-memory location.

## Set Bit of Word (SETB)

An output that turns on a single bit of a V-memory location. The bit remains on until it is reset. The rest bit of word instruction is used to turn off the bit.

## Reset Bit of Word (RSTB)

An output that resets a single bit of a V-memory location.

## Immediate Instructions

- Store Immediate (STR I)**  
Begins a rung/branch of logic with a normally open contact. The contact will be updated with the current input field status when processed in the program scan.
- Store Not Immediate (STR NOT I)**  
Begins a rung/branch of logic with a normally closed contact. The contact will be updated with the current input field status when processed in the program scan.
- Or Immediate (OR I)**  
Connects a normally open contact in parallel with another contact. The contact will be updated with the current input field status when processed in the program scan.
- Or Not Immediate (OR NOT I)**  
Connects a normally closed contact in parallel with another contact. The contact will be updated with the current input field status when processed in the program scan.
- And Immediate (AND I)**  
Connects a normally open contact in series with another contact. The contact will be updated with the current input field status when processed in the program scan.
- And Not Immediate (AND NOT I)**  
Connects a normally closed contact in series with another contact. The contact will be updated with the current input field status when processed in the program scan.
- Out Immediate (OUT I)**  
Reflects the status of the rung. The output field device status is updated when the instruction is processed in the program scan.
- Or out immediate (OR OUT)**  
Reflects the status of the rung and outputs the discrete (ON/OFF) state to the image register. Multiple OR OUT instructions referencing the same discrete point can be used in the program. The output field device status is updated when the instruction is processed in the program scan.
- Set Immediate (SET I)**  
An output that turns on a point or a range of points. The reset instruction is used to turn the point(s) off that were set. The output field device status is updated when the instruction is processed in the program scan.
- Reset Immediate (RST I)**  
An output that resets a point or a range of points. The output field device status is updated when the instruction is processed in the program scan.

## Timer, Counter, and Shift Register Instructions

- Timer (TMR)**  
Single input incrementing timer with 0.1 second resolution (0-999.9 seconds).
- Fast Timer (TMRF)**  
Single input incrementing timer with 0.01 second resolution (0-99.99 seconds).
- Accumulating Timer (TMRA)**  
Two input incrementing timer with 0.1 second resolution (0-9,999,999.9 sec.). Time enable/reset inputs control the timer.
- Accumulating Fast Timer (TMR AF)**  
Two input incrementing timer with 0.01 second resolution (0-999,999.99 sec.). Time and enable/reset inputs control timer.
- Counter (CNT)**  
Two input incrementing counter (0-9999). Count and reset inputs control the counter.
- Stage Counter (SGCNT)**  
Single input incrementing counter (0-9999). RST instruction must be used to reset count.
- Up Down Counter (UDC)**  
Three input counter (0-99999999). Up, down, and reset inputs control the counter.
- Shift Register (SR)**  
Shifts data through a range of control relays with each clock pulse. The data, clock, and reset inputs control the shift register.

## Accumulator / Stack Load and Output Data

- Load (LD)**  
Loads a 16-bit word into the lower 16-bits of the accumulator / stack.
- Load Double (LDD)**  
Loads a 32-bit word into the accumulator / stack.
- Load Real Number (LDR)**  
Loads a real number contained in two consecutive V-memory locations or an 8-digit constant into the accumulator.
- Load Formatted (LDF)**  
Loads the accumulator with a specified number of consecutive discrete memory bits.
- Load Address (LDA)**  
Loads the accumulator with the HEX value for an octal constant (address).
- Load Accumulator Indexed (LDX)**  
Loads the accumulator with a V-memory address to be offset by the value in the accumulator stack.
- Load Accumulator Indexed from Data Constants (LDSX)**  
Loads the accumulator with an offset constant value (ACON/NCON) from a data label area (DLBL).
- Out (OUT)**  
Copies the value in the lower 16-bits of the accumulator to a specified V memory location.
- Out Double (OUTD)**  
Copies the value in the accumulator to two consecutive V-memory locations.
- Out Formatted (OUTF)**  
Outputs a specified number of bits (1-32) from the accumulator to the specified discrete memory locations.

## Output Indexed (OUTX)

Copies a 16-bit value from the first level of the accumulator stack to a source address offset by the value in the accumulator.

## Pop (POP)

Moves the value from the first level of the accumulator stack to the accumulator and shifts each value in the stack up one level.

## Logical Instructions (Accumulator)

- And (AND)**  
Logically ANDS the lower 16 bits in the accumulator with a V memory location.
- And Double (ANDD)**  
Logically ANDS the value in the accumulator with an 8 digit constant.
- And Formatted (ANDF)**  
Logically ANDS the value in the accumulator and a specified range of discrete memory bits (1-32).
- Or (OR)**  
Logically ORS the lower 16-bits in the accumulator with a V-memory location.
- Or Double (ORD)**  
Logically ORS the value in the accumulator with an 8-digit constant.
- Or Formatted (ORF)**  
Logically ORS the value in the accumulator with a range of discrete bits (1-32).
- Exclusive Or (XOR)**  
Performs an Exclusive OR of the value in the lower 16-bits of the accumulator and a V-memory location.
- Exclusive Or Double (XORD)**  
Performs an Exclusive OR of the value in the accumulator and an 8 digit constant.
- Exclusive Or Formatted (XORF)**  
Performs an exclusive OR of the value in the accumulator and a range of discrete bits (1-32).
- Compare (CMP)**  
Compares the value in the lower 16 bits of the accumulator with a V-memory location.
- Compare Double (CMPD)**  
Compares the value in the accumulator with two consecutive V-memory locations or an 8-digit constant.
- Compare Formatted (CMPF)**  
Compares the value in the accumulator with a specified number of discrete bits (1-32).
- Compare Real Number (CMPR)**  
Compares the real number in the accumulator with two consecutive V-memory locations or an 8-digit real number constant.

# D3-350 INSTRUCTION SET

## Math Instructions (Accumulator)

### Add (ADD)

Adds a BCD value in the lower 16-bits in the accumulator with a V-memory location. The result resides in the accumulator.

### Add Double (ADDD)

Adds a BCD value in the accumulator with two consecutive V-memory locations or an 8-digit constant. The result resides in the accumulator.

### Add Real Number (ADDR)

Adds a real number in the accumulator with a real number constant or a real number contained in two consecutive V-memory locations. The result resides in the accumulator.

### Subtract (SUB)

Subtracts a BCD value, which is either a V-memory location or a 4-digit constant, from the lower 16-bits in the accumulator. The result resides in the accumulator.

### Subtract Double (SUBD)

Subtracts a BCD value, which is either two consecutive V-memory locations or an 8-digit constant, from a value in the accumulator. The result resides in the accumulator.

### Subtract Real Number (SUBR)

Subtracts a real number, which is either two consecutive V-memory locations or a real number constant, from the real number in the accumulator. The result resides in the accumulator.

### Multiply (MUL)

Multiplies a BCD value, which is either a V-memory location or a 4-digit constant, by the value in the lower 16-bits in the accumulator. The result resides in the accumulator.

### Multiply Double (MULD)

Multiplies a BCD value contained in two consecutive V-memory locations by the value in the accumulator. The result resides in the accumulator.

### Multiply Real Number (MULR)

Multiplies a real number, which is either two consecutive V-memory locations or a real number constant, by the real number in the accumulator. The result resides in the accumulator.

### Divide (DIV)

Divides a BCD value in the lower 16-bits of the accumulator by a BCD value which is either a V-memory location or a 4-digit constant. The result resides in the accumulator.

### Divide Double (DIVD)

Divides a BCD value in the accumulator by a BCD value which is either two consecutive V-memory locations or an 8-digit constant. The result resides in the accumulator.

### Divide Real Number (DIVR)

Divides a real number in the accumulator by a real number which is either two consecutive V-memory locations or a real number constant. The result resides in the accumulator.

### Add Binary (ADDB)

Adds the binary value in the lower 16 bits of the accumulator to a value which is either a V-memory location, or a 16-bit constant. The result resides in the accumulator.

### Subtract Binary (SUBB)

Subtracts a 16-bit binary value, which is either a V-memory location or a 16-bit constant, from the lower 16 bits in the accumulator. The result resides in the accumulator.

### Multiply Binary (MULB)

Multiplies a 16-bit binary value, which is either a V-memory location or a 16-bit constant, by the lower 16 bits in the accumulator. The result resides in the accumulator.

### Divide Binary (DIVB)

Divides the binary value in the lower 16 bits in the accumulator by a value which is either a V-memory location or a 16-bit constant. The result resides in the accumulator.

### Increment (INC)

Increments a BCD value in a specified V-memory location by 1 each time the instruction is executed.

### Decrement (DEC)

Decrements a BCD value in a specified V-memory location by 1 each time the instruction is executed.

### Increment Binary (INCB)

Increments a binary value in a specified V-memory location by 1 each time the instruction is executed.

### Decrement Binary (DECB)

Decrements a binary value in a specified V-memory location by 1 each time the instruction is executed.

## Bit Instructions (Accumulator)

### Sum (SUM)

Counts the number of bits in set to "1" in the accumulator. The HEX result resides in the accumulator.

### Shift Left (SHFL)

Shifts the bits in the accumulator a specified number of places to the left.

### Shift Right (SHFR)

Shifts the bits in the accumulator a specified number of places to the right.

### Rotate Left (ROTL)

Rotates the bits in the accumulator a specified number of places to the left.

### Rotate Right (ROTR)

Rotates the bits in the accumulator a specified number of places to the right.

### Encode (ENCO)

Encodes the bit position set to 1 in the accumulator, and returns the appropriate binary representation in the accumulator.

### Decode (DECO)

Decodes a 5-bit binary value (0-31) in the accumulator by setting the appropriate bit position to 1 in the accumulator.

## Number Conversion Instructions (Accumulator)

### Binary (BIN)

Converts the BCD value in the accumulator to the equivalent binary value. The result resides in the accumulator.

### Binary Coded Decimal (BCD)

Converts the binary value in the accumulator to the equivalent BCD value. The result resides in the accumulator.

### Invert (INV)

Takes the one's complement of the 32-bit value in the accumulator. The result resides in the accumulator.

### Ten's complement (BCDCPL)

Takes the ten's complement of the BCD value in the accumulator. The result resides in the accumulator.

### ASCII to HEX (ATH)

Converts the table of ASCII values to a table of hexadecimal values.

### HEX to ASCII (HTA)

Converts a table of hexadecimal values to a table of ASCII values.

### Segment (SEG)

Converts a 4-digit HEX number in the accumulator to a corresponding bit pattern for interfacing to seven segment displays. The result resides in the accumulator.

### Gray code to BCD (GRAY)

Converts a 16-bit GRAY code value in the accumulator to a corresponding BCD value. The result resides in the accumulator.

### Shuffle digits (SFLDGT)

Shuffles a maximum of 8 digits rearranging them in a specified order. The result resides in the accumulator.

### Binary to Real Number (BTOR)

Converts the integer value in the accumulator into a real number. The result resides in the accumulator.

### Real Number to Binary (RTOB)

Converts the real number in the accumulator into an integer value. The result resides in the accumulator.

## Table Instructions

### Move (MOV)

Moves the values from on V-memory table to another V-memory table.

### Move Memory Cartridge/Load Label (MOVMC/LDBL)

Copies data from data label area in program ladder memory to V-memory.

### Move Memory Cartridge/Load Label (MOVMC/LDLBL)

Copies data between V-memory and program ladder memory.

## Clock/Calendar Instructions

### Date (DATE)

Sets the date (year, month, day, day of the week) in the CPU calendar using two consecutive V memory locations.

### Time (TIME)

Sets the time (hour, seconds, and minutes) in the CPU using two consecutive V-memory locations.

## CPU Control Instructions

### No Operation (NOP)

Inserts a no operation coil at a specified program address.

### End (END)

Marks the termination point for the normal program scan. An End instruction is required at the end of the main program body.

### Stop (STOP)

Changes the operational mode of the CPU from Run to Program (Stop).

### Reset Watchdog Timer (RSTWT)

Resets the CPU watchdog timer.

## Program Control Instructions

### Goto/Label (GOTO/LBL)

Skips (does not execute) all instructions between the GOTO and the corresponding label (LBL) instruction.

### For/Next (FOR/NEXT)

Executes the logic between the FOR and NEXT instructions a specified number of times.

### Goto Subroutine/Subroutine Return

### Conditional/Subroutine Return (GTS/SBR w/RT)

When a GTS instruction is executed, the program jumps to the SBR (subroutine). The subroutine is terminated with an RT instruction (unconditional return). When a return is executed, the program continues from the instruction after the calling GTS instruction.

### Master Line Set/Master Line Reset (MLS/MLR)

Allows the program to control sections of ladder logic by forming a new power rail. The MLS marks the beginning of a power rail and the MLR marks the end of the power rail control.

## Interrupt Instructions

### Interrupt Routine/Interrupt Return/Interrupt Return

### Conditional (INT/IRT/IRTC)

When a hardware or software interrupt occurs, the interrupt routine will be executed. The INT instruction is the beginning of the interrupt routine. The interrupt routine is terminated with an IRT instruction (unconditional interrupt return). When an interrupt return is reached, the execution of the program continues from the instruction where the program execution was prior to the interrupt.

### Enable Interrupt (ENI)

Enables hardware and software interrupts to be acknowledged.

### Disable Interrupt (DISI)

Disables hardware and software interrupts from being acknowledged.

## Intelligent Module Instructions

### Read from Intelligent Module (RD)

Reads a block of data (1-128 bytes max.) from an intelligent I/O module.

### Write to Intelligent Module (WT)

Writes a block of data (1-28 bytes max.) to an intelligent I/O module.

## Network Instructions

### Read from network (RX)

Reads a block of data from another CPU on the network.

### Write to network (WX)

Writes a block of data from the master device to a slave device on the network.

## Message Instructions

### Fault/Data Label (FAULT/DLBU)

Displays a V-memory value or a Data label constant to the handheld programmer or personal computer using DirectSOFT.

### Numerical Constant/ASCII constant (NCON/ACON)

Stores constants in numerical or ASCII form for use with other instructions.

### Print Message (PRINT)

Prints the embedded text or text/data variable message to the specified communications port. Maximum message length is 255 words.

## RLL PLUS Programming Instructions

### Initial stage (ISG)

The initial stage instruction is used as a starting point for the user application program. The ISG instruction will be active on power up and PROGRAM to RUN transitions.

### Stage (SG)

Stage instructions are used to create structured programs. They are program segments which can be activated or deactivated with control logic.

### Jump (JMP)

Normally open coil that deactivates the active stage and activates a specified stage when there is power flow to the coil.

### Not Jump (NJMP)

Normally closed coil that deactivates the active stage and activates a specified stage when there is no power flow to the coil.

### Converge stages (CV)

Converge stages are a group of stages that when all stages are active the associated converge jump(s) (CVJMP) will activate another stage(s). One scan after the CVJMP is executed, the converge stages will be deactivated.

### Converge Jump (CVJMP)

Normally open coil that deactivates the active CV stages and activates a specified stage when there is power flow to the coil.

### Block Call/Block/Block End (BCALL and BEND)

BCALL is a normally open coil that activates a block of stages when there is power flow to the coil. BLK is the label that marks the beginning of a block of stages. BEND is a label used to mark the end of a block of stages.

## Drum Instructions

### Timed Drum with Discrete Outputs (DRUM)

Time driven drum with up to 16 steps and 16 discrete output points. Output status is written to the appropriate output during each step. Specify a time base per count (in milliseconds). Each step can have a different number of counts to trigger the transition to the next step. Also define preset step as destination when reset occurs.

### Time & Event Drum with Discrete Outputs (EDRUM)

Time and/or event driven drum with up to 16 steps and 16 discrete output points. Output status is written to the appropriate output during each step. Specify a time base per count (in milliseconds). Each step can have a different number of counts and an event to trigger counting. Once the time has expired, a transition to the next step occurs. Also define preset step as destination when reset occurs.

### Time & Event Drum with Discrete Outputs & Output Mask (MDRUMD)

Time and/or event driven drum with up to 16 steps and 16 discrete output points. Actual output status is the result of a bit-by-bit AND between the output mask and the bit mask in the step. Specify a time base per count (in milliseconds). Each step can have a different number of counts and an event to trigger counting. Once the time has expired, a transition to the next step occurs. Also define preset step as destination when reset occurs.

### Time & Event Drum with Word Output & Output Mask (MDRUMW)

Time and/or event driven drum with up to 16 steps and a single V-memory output location. Actual output word is the result of a bit-by-bit AND between the word mask and the bit mask in the step. Specify a time base per count (in milliseconds). Each step can have a different number of counts and an event to trigger counting. Once the time has expired, a transition to the next step occurs. Also define preset step as destination when reset occurs.